


GA-790FXTA-UD5

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25	AUDIO JACK	

[illegible]

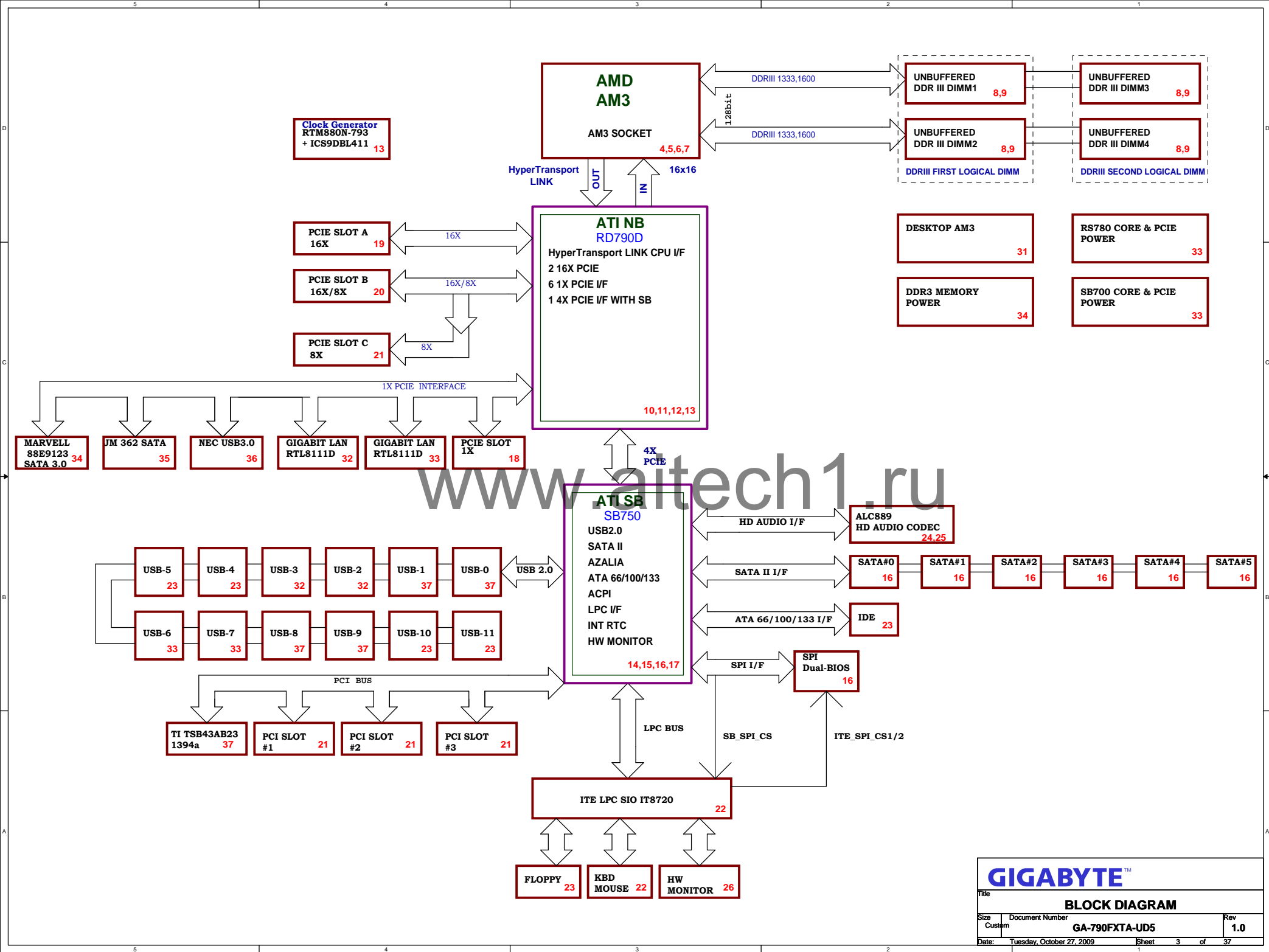
			
Title			
COVER SHEET			
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AM2->AM3 Different: AE9->AE7,
H22->H20. Extra pin:B2 AM3 socket is
938 pins. Only shift 2 pins.

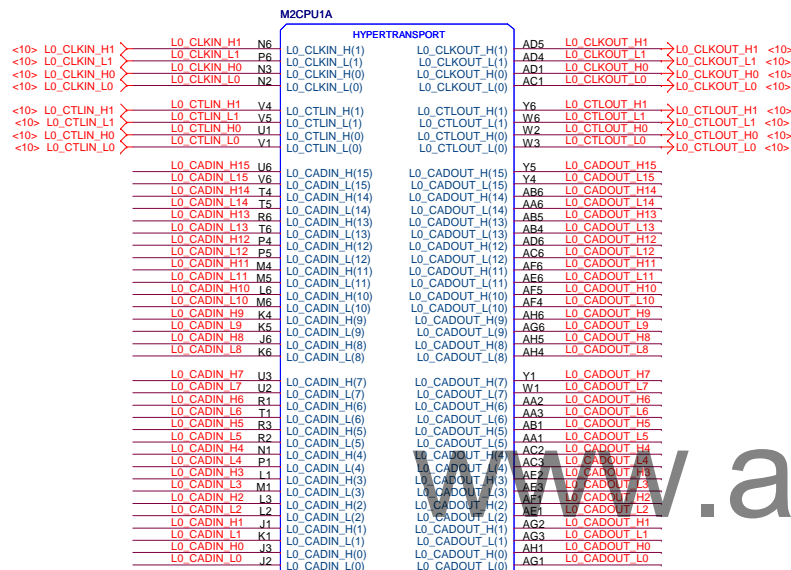
Version: 1.0

6 Layer, 5mil 60ohm +/- 15% O P-Code: U98093-0

[illegible][illegible]



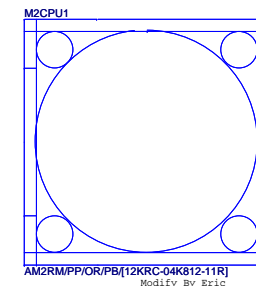
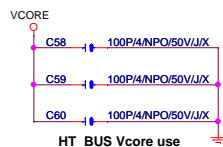
L0_CADIN_L[0..15] <L0_CADIN_L[0..15] <10>
 L0_CADIN_H[0..15] <L0_CADIN_H[0..15] <10>
 L0_CADOUT_L[0..15] <L0_CADOUT_L[0..15] <10>
 L0_CADOUT_H[0..15] <L0_CADOUT_H[0..15] <10>



CPU-SK941AM3S/GF[10SC1-A01941-01R]

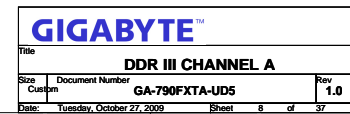
CPU_VDD_RUN = VCORE
 CPU_VDDA_RUN = VDDA25
 VLDT_RUN = VCC12_HT
 CPU_VDDIO_SUS = DDR15V
 CPU_VDDR = CPU_VDDR12

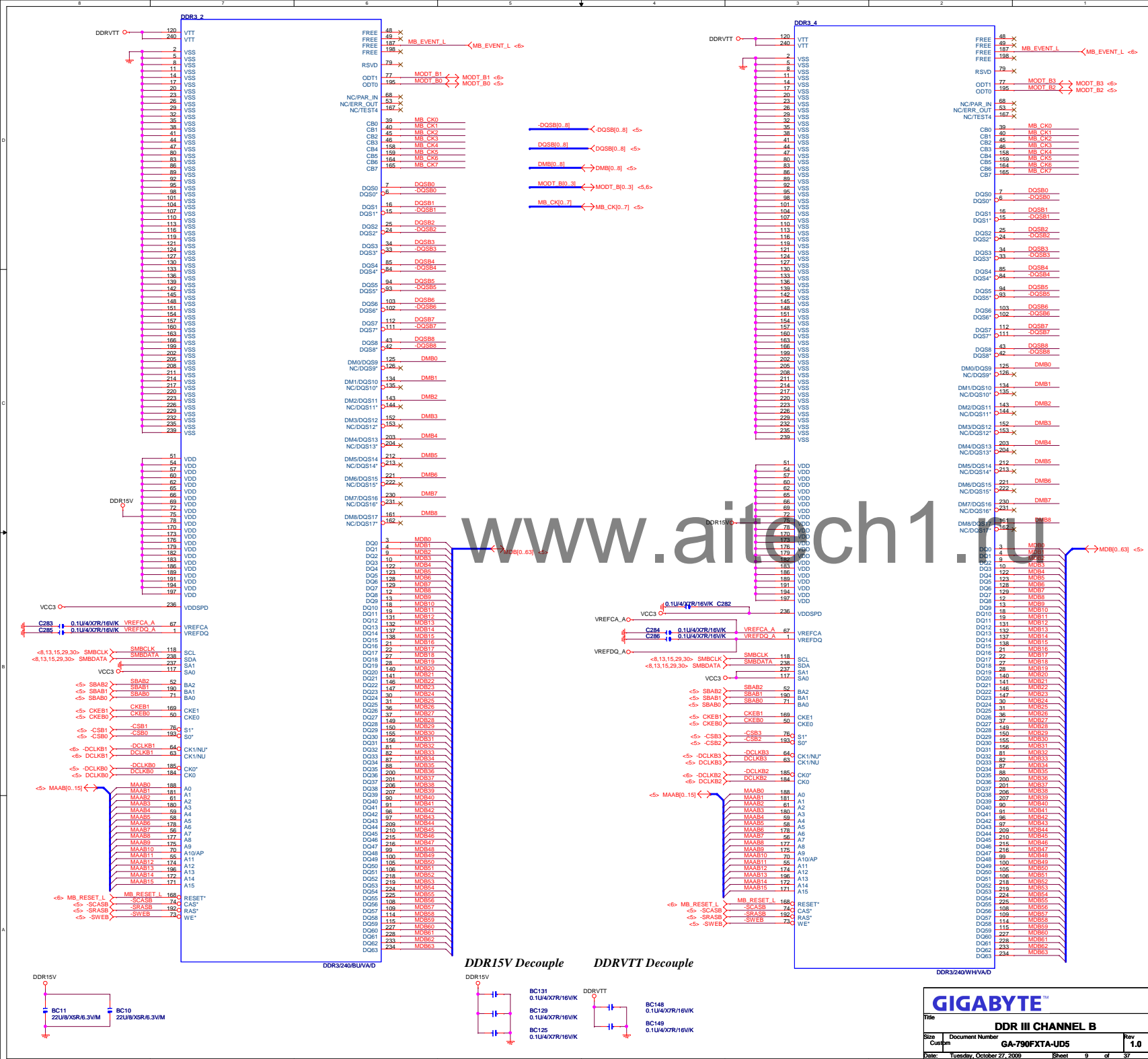
VLDT_A = VCC12_HT
 VLDT_B = HT12B

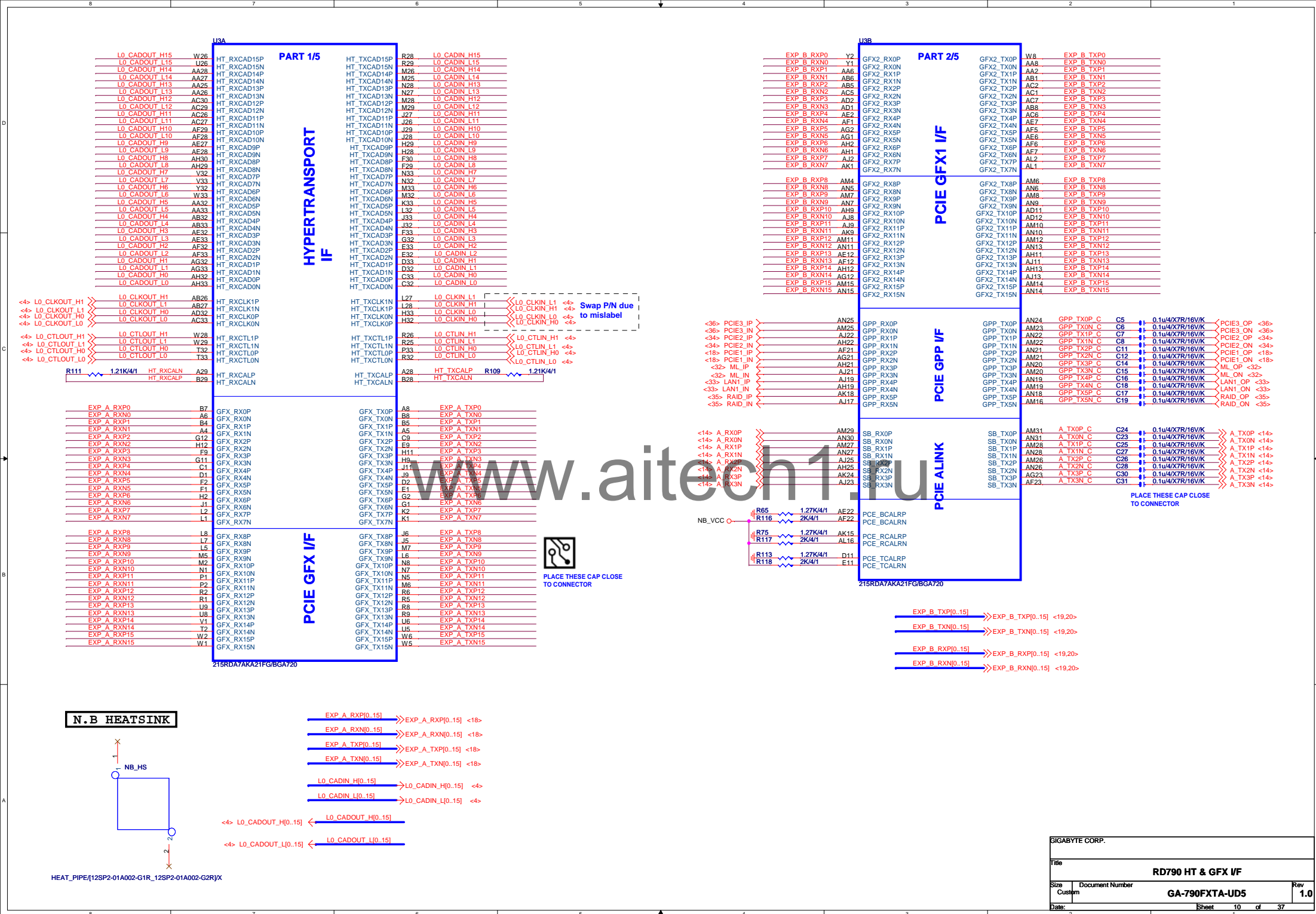


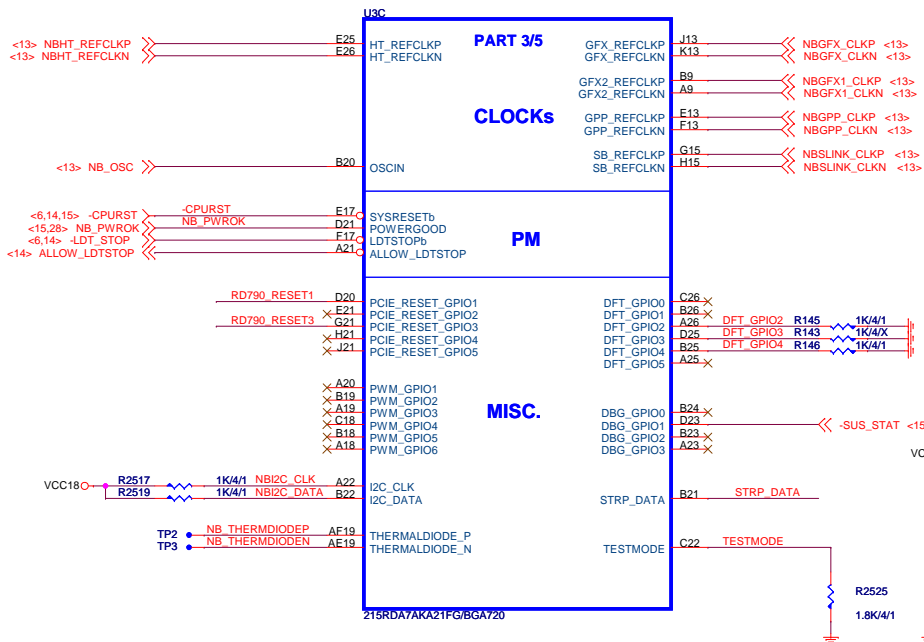
GIGABYTE™

Title			
CPU HYPER TRANSPORT			
Size	Document Number	Rev	
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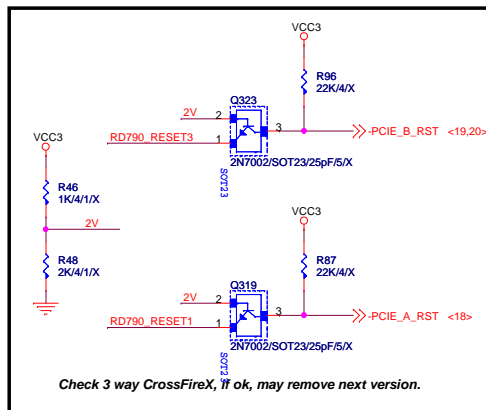


DFT_GPIO[4:2]: STRAP_PCIE_GPP_CFG[2:0]

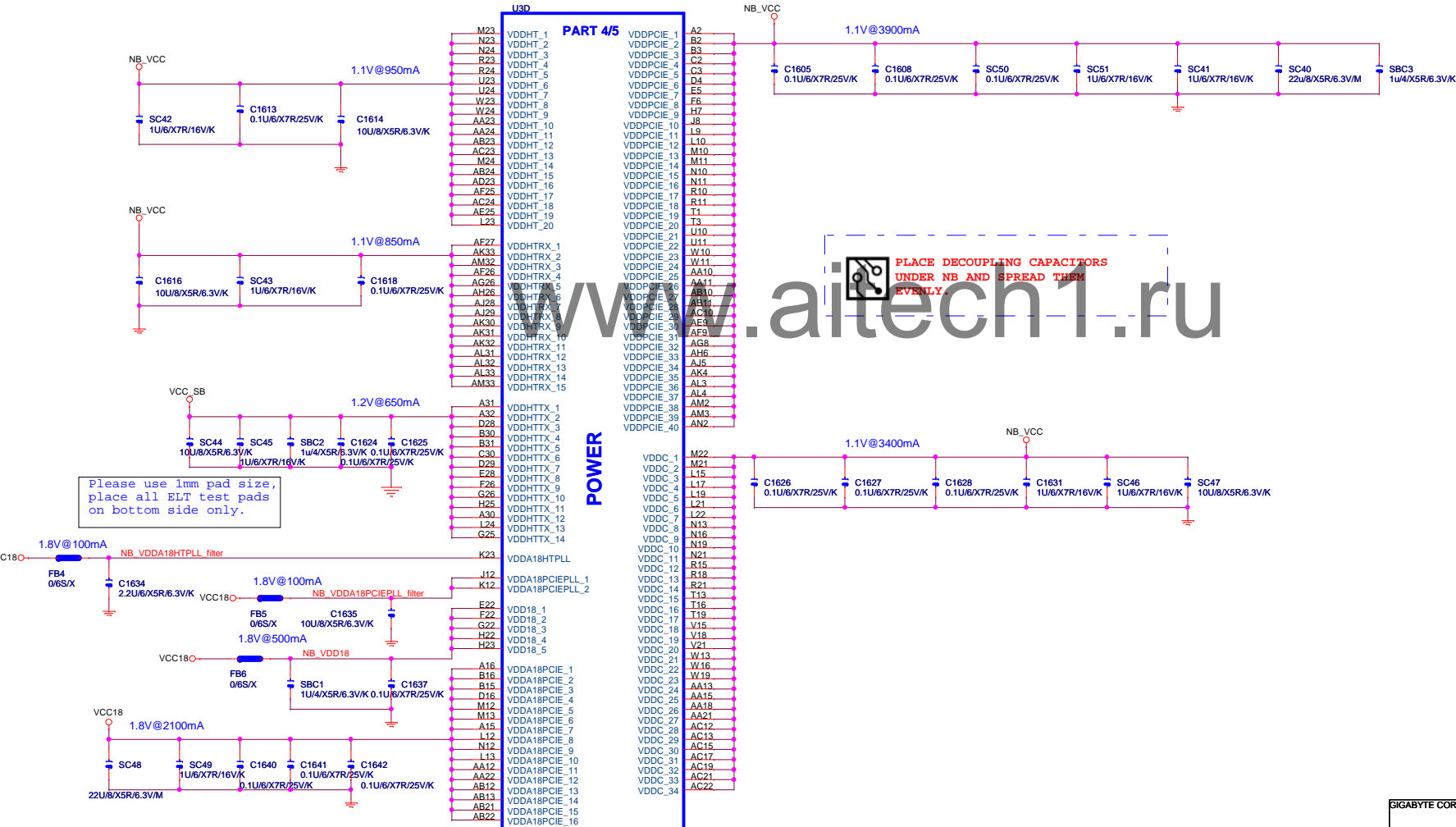
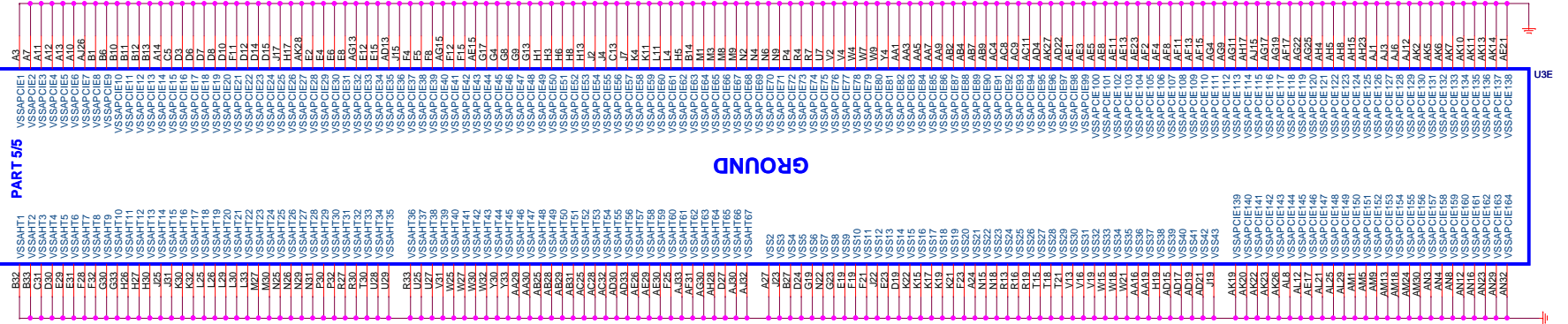
These pin straps are used to configure PCI-E GPP mode.
(GPIO4, GPIO3, GPIO2)

000 : 00001 (4 + 2)
 001 : 00010 (4 + 1 + 1)
 010 : 01011 (1 + 1 + 1 + 1 + 1 + 1)
 011 : 00100 (2 + 1 + 1 + 1 + 1)
 100 : 01010 (2 + 2 + 1 + 1)
 101 : 01100 (2 + 2 + 2)
 111 : 01011 (1 + 1 + 1 + 1 + 1 + 1) or EEPROM

All of the above straps are active low straps and are pulled up internally to work in default states. It is not required to connect any external pull up to these straps. In order to change the default values of the strap, the strap pins must be pulled down to VSS through a resistor (typically 3 Kohm).



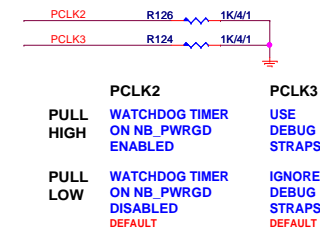
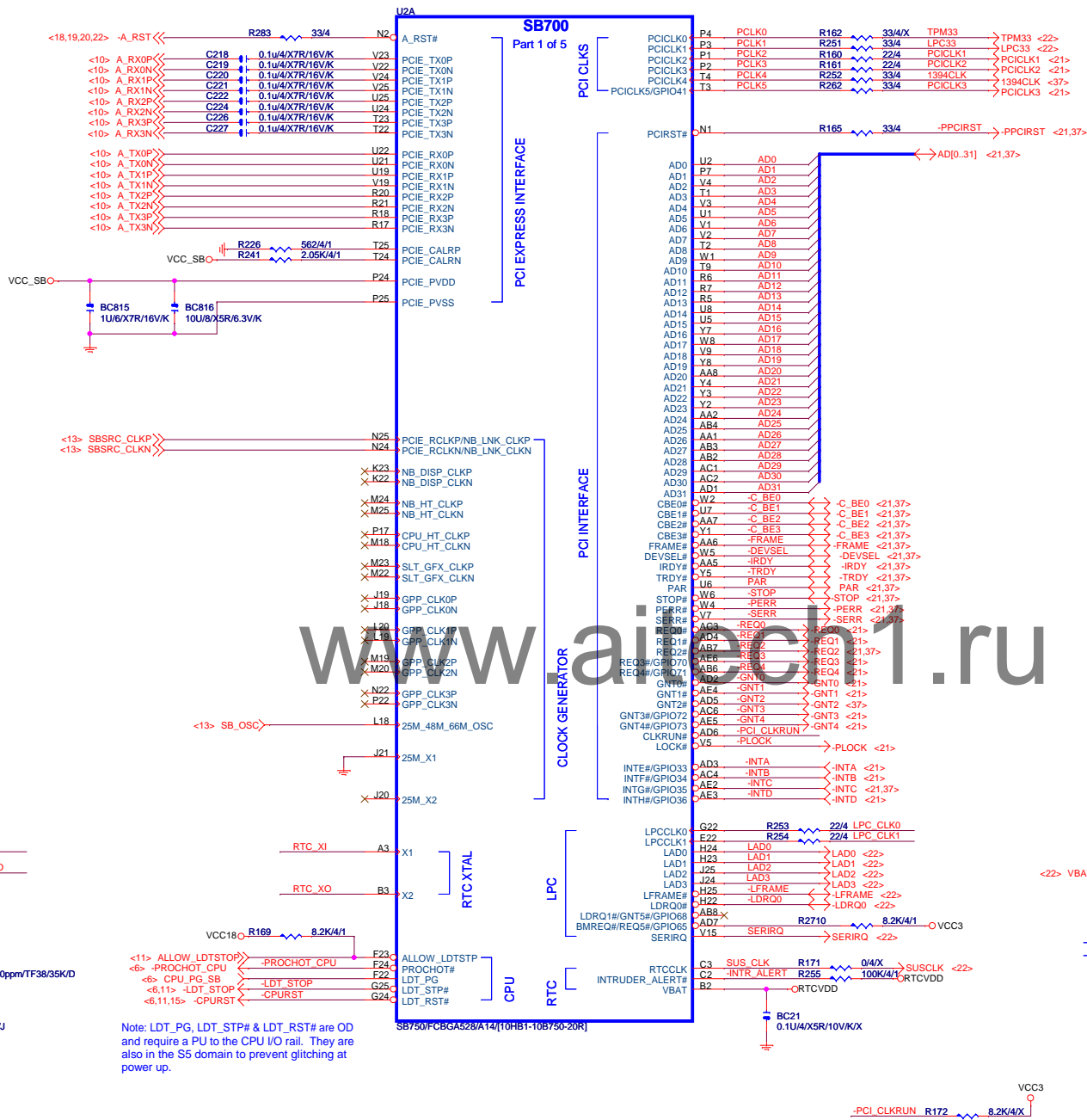
For CrossFire, 兩張顯示卡的reset timing 不同。需要用GPIO pin來control





A diagram of a square with vertices labeled 1, 2, 3, 4. The label SB_{HS} is placed near the top-left vertex.

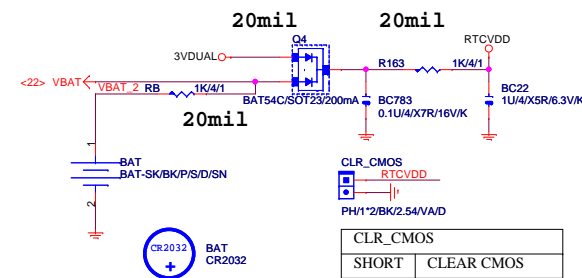
SB_HS/12SP2-030030-21R_12SP2-030030-22R\X



BIOS after boot setting
EC AOD-ACC



	LPC_CLK0 Rev.AT2	LPC_CLK1
PULL HIGH	IMC ENABLED	CLKGEN ENABLED
PULL LOW	AOD Extreme IMC DISABLED DEFAULT	CLKGEN DISABLED DEFAULT



NOT ADD ICT FOR RTCVDD PIN

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Title **ATI SB750 PCIE/PCI/CPU/LPC**

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SB TEST2 R69 8.2K/4/1
SB TEST1 R70 8.2K/4/1
SB TEST0 R72 8.2K/4/1

-SUS_STAT R208 8.2K/4/1
SMBCLK R78 1K/4/1
SMBDATA R79 1K/4/1
WD_PWRGD R81 8.2K/4/1

-RI R187 8.2K/4/1
SMBCLK1 R173 2.2K/4/1
SMBDATA1 R181 2.2K/4/1
-PCIPME R209 2.2K/4/1
-PCIE_WAKE R211 2.2K/4/1

SB_PWROK R82 20K/4/1
C1064 100P/4/N/50V/JX
C1050 100P/4/N/50V/X
C1051 100P/4/N/50V/X

SMBCLK1 C1052 100P/4/N/50V/X
SMBDATA1 C1053 100P/4/N/50V/X

AZ_BIT_CLK C1063 22P/4/N/50V/JX

-AZ_RST R77 8.2K/4/1

AZ_RST# Rev.A12
PULL HIGH ENABLE PCI
PULL LOW DISABLE PCI

MEM BOOT
MEM BOOT
DEFAULT

DDR15V R240 8.2K/4/1

IMC_DBREQ- Q44 MMBT3904/SOT23/200mA/30

DDR15V R248 8.2K/4/1

IMC_DBRDY Q45 MMBT3904/SOT23/200mA/30

C108 22P/4/N/50V/X
R164 22/4

3VDUAL R82 20K/4/1
BC28 2.2U/6/X5R/6.3V/K

3VDUAL R82 20K/4/1

3VDUAL R82 20K/4/1

3VDUAL R82 20K/4/1

3VDUAL R82 20K/4/1

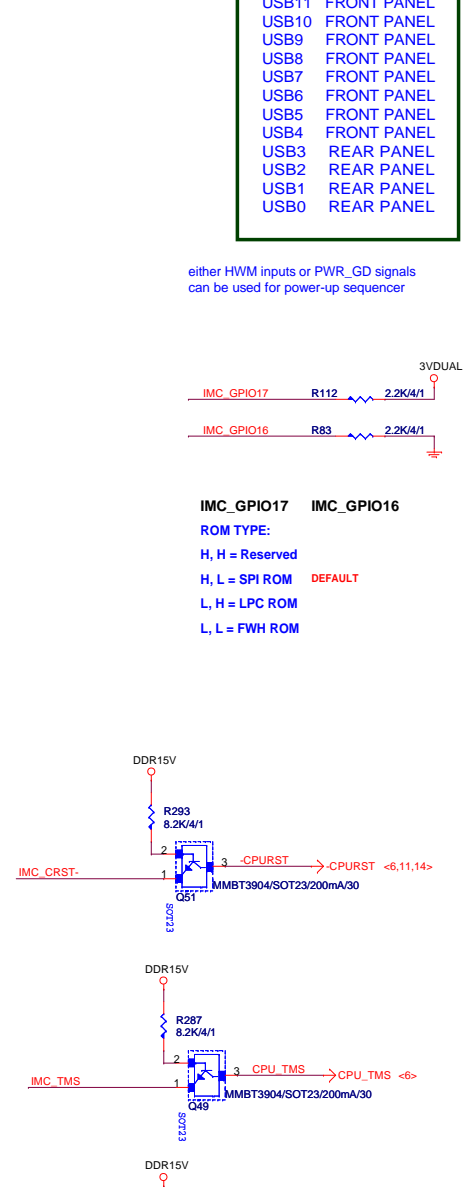
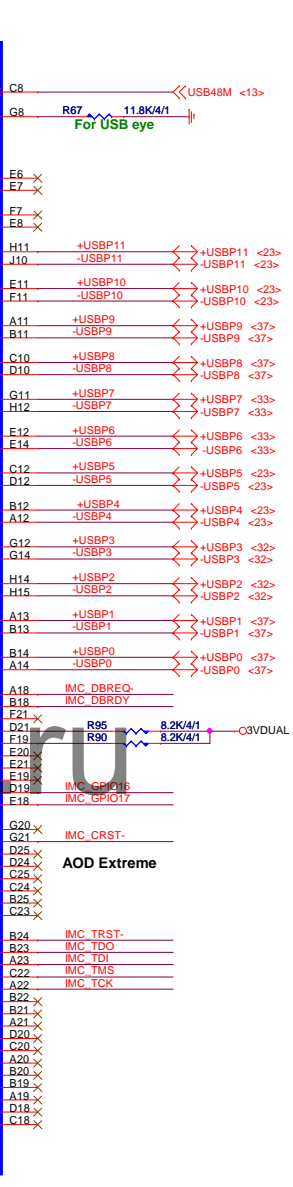
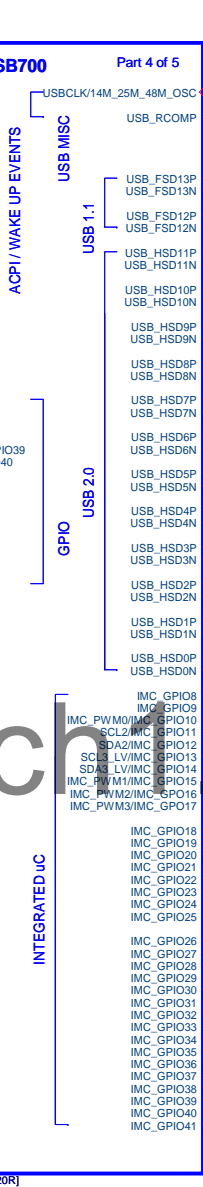
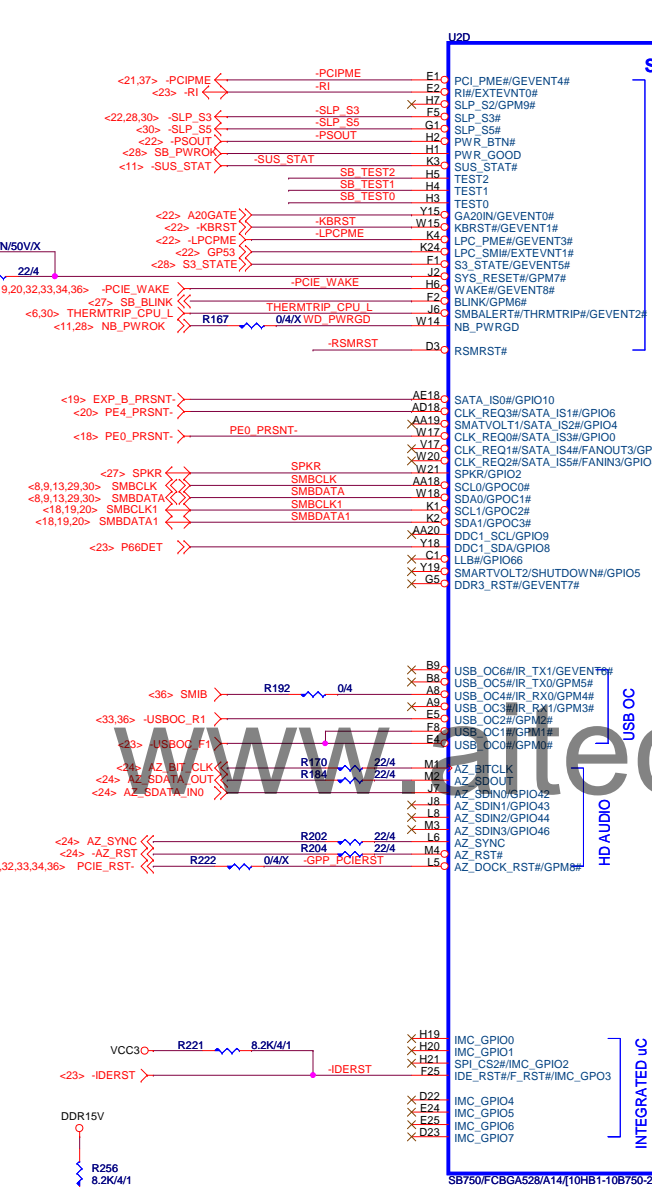
3VDUAL R82 20K/4/1

3VDUAL R82 20K/4/1

3VDUAL R82 20K/4/1

3VDUAL R82 20K/4/1

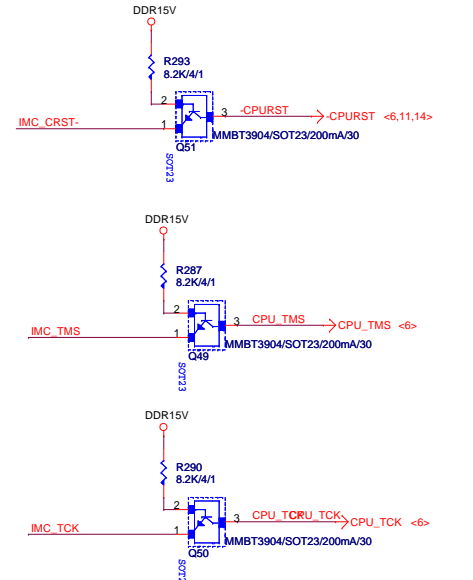
3VDUAL R82 20K/4/1



- USB11 FRONT PANEL
- USB10 FRONT PANEL
- USB9 FRONT PANEL
- USB8 FRONT PANEL
- USB7 FRONT PANEL
- USB6 FRONT PANEL
- USB5 FRONT PANEL
- USB4 FRONT PANEL
- USB3 REAR PANEL
- USB2 REAR PANEL
- USB1 REAR PANEL
- USB0 REAR PANEL

either HWM inputs or PWR_GD signals can be used for power-up sequencer

IMC_GPIO17 IMC_GPIO16
ROM TYPE:
H, H = Reserved
H, L = SPI ROM DEFAULT
L, H = LPC ROM
L, L = FWH ROM





PLACE SATA_CAL
RES VERY CLOSE
TO BALL OF U600

NOTE:
R650 IS 1K 1% FOR 25MHz
XTAL, 4.99K 1% FOR 100MHz
INTERNAL CLOCK

<23> -SATA_LED

-SATA_LED

VCC_SB

VCC3

W12

XTLVDD_SATA

W12

W12

W12

W12

W12

W12

W12

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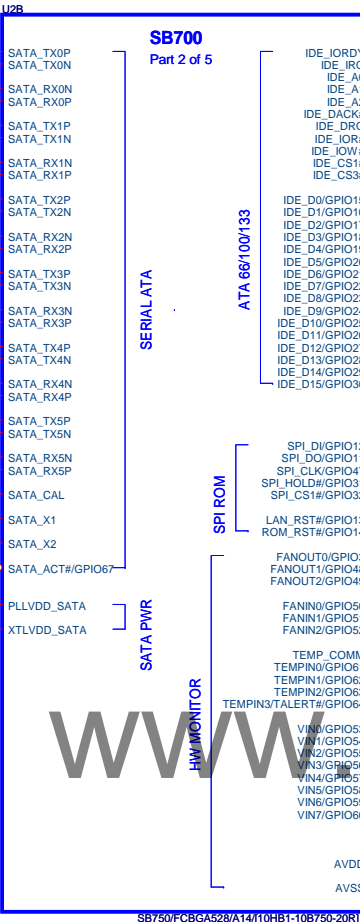
W12

W12

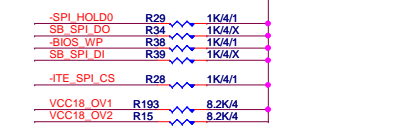
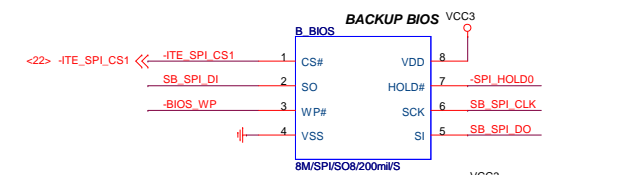
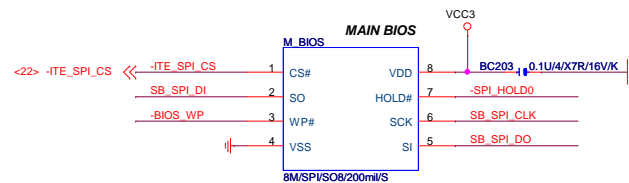
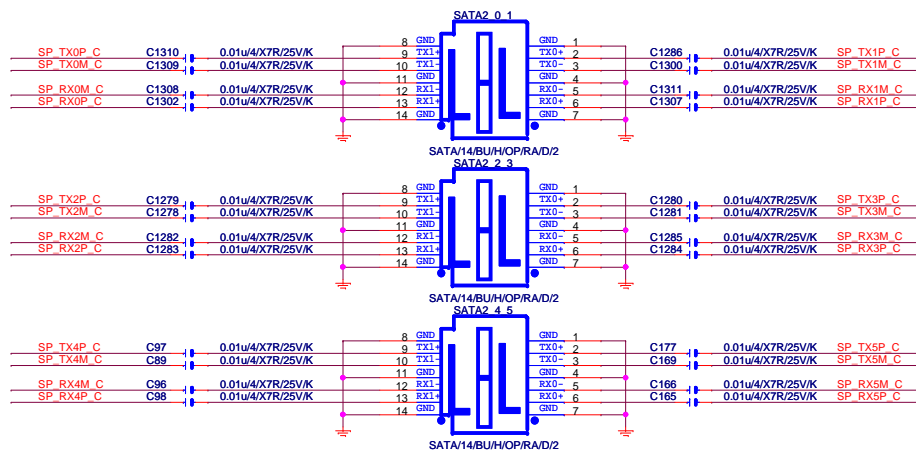
W12

W12

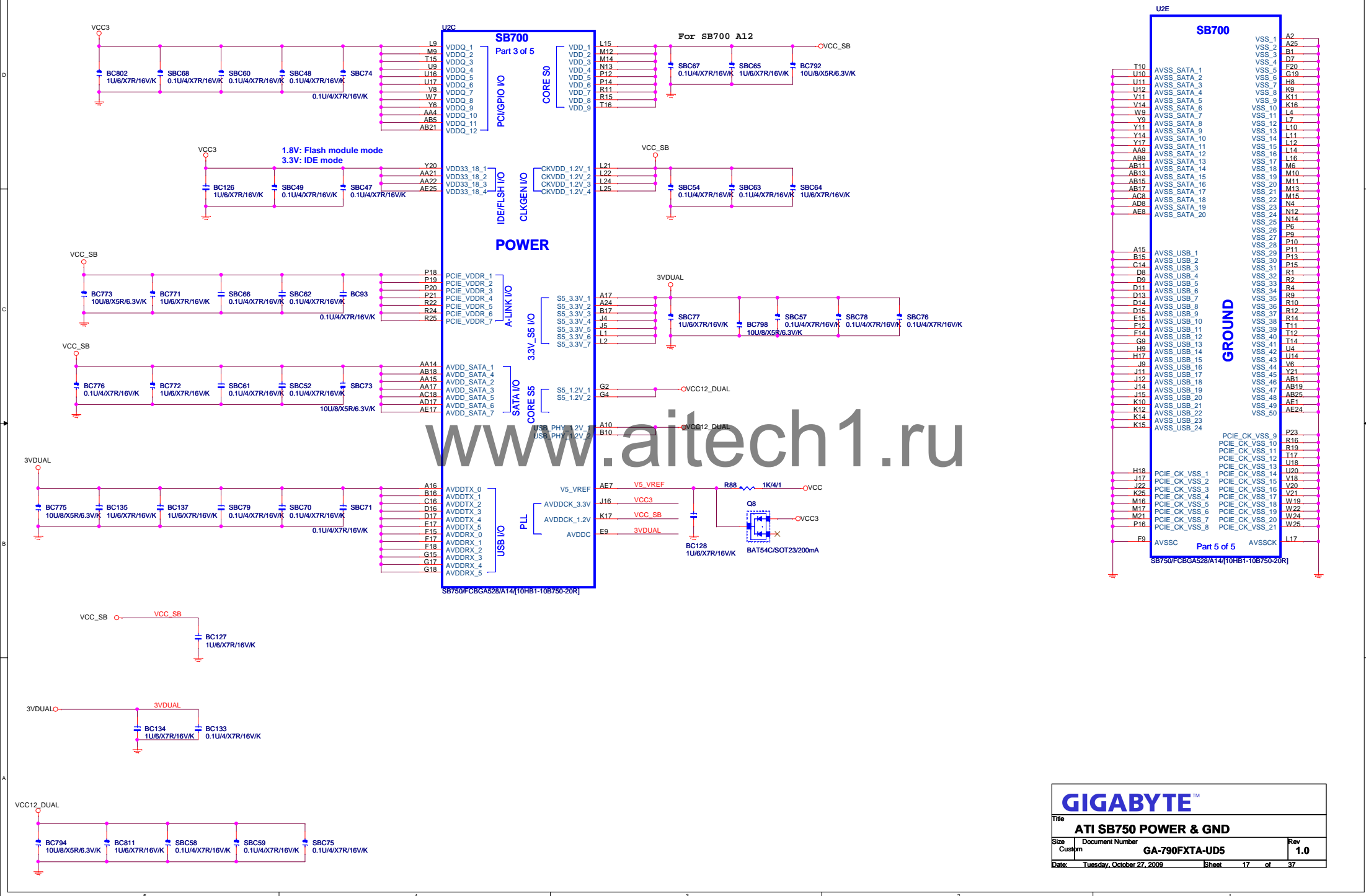
W12

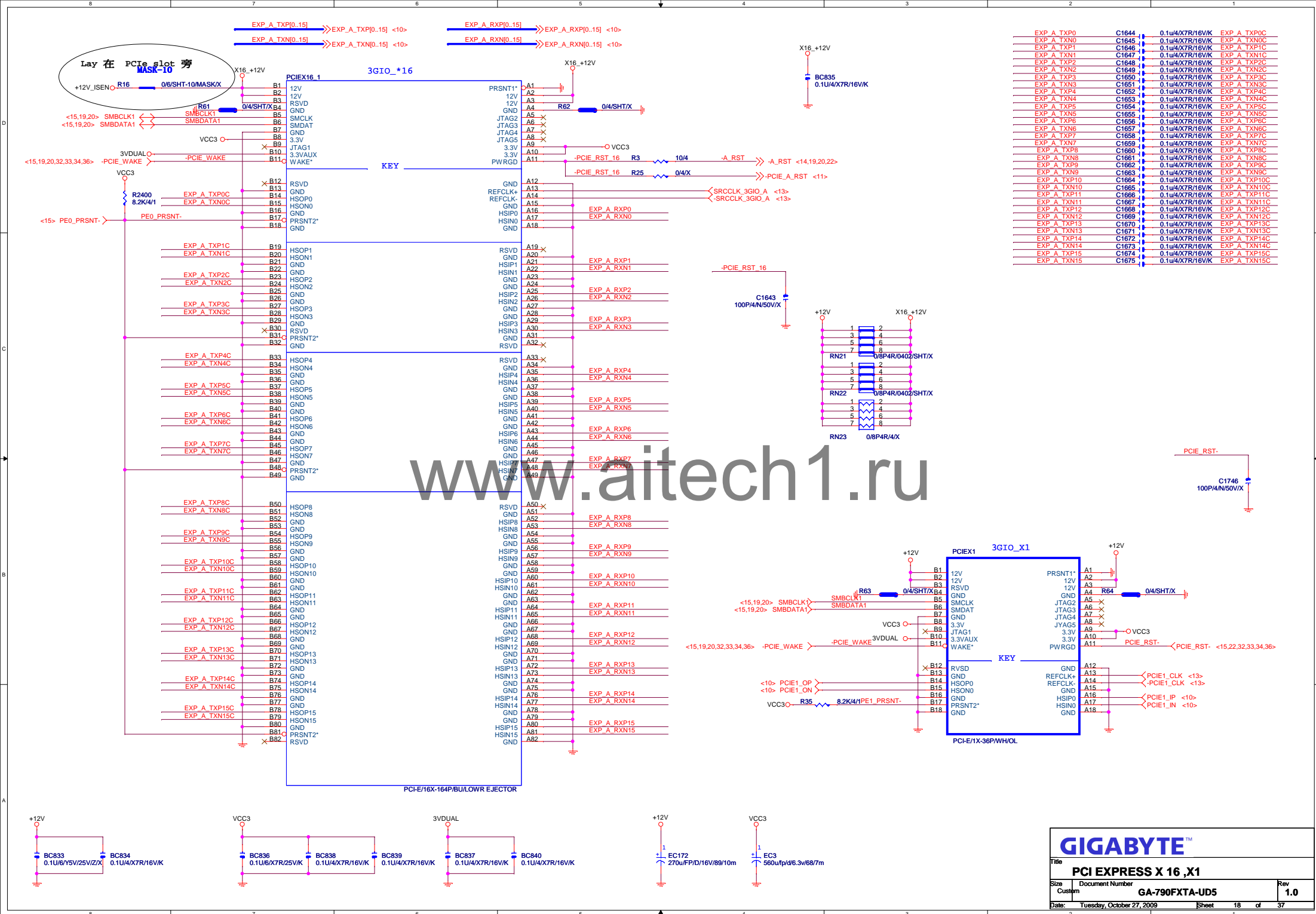


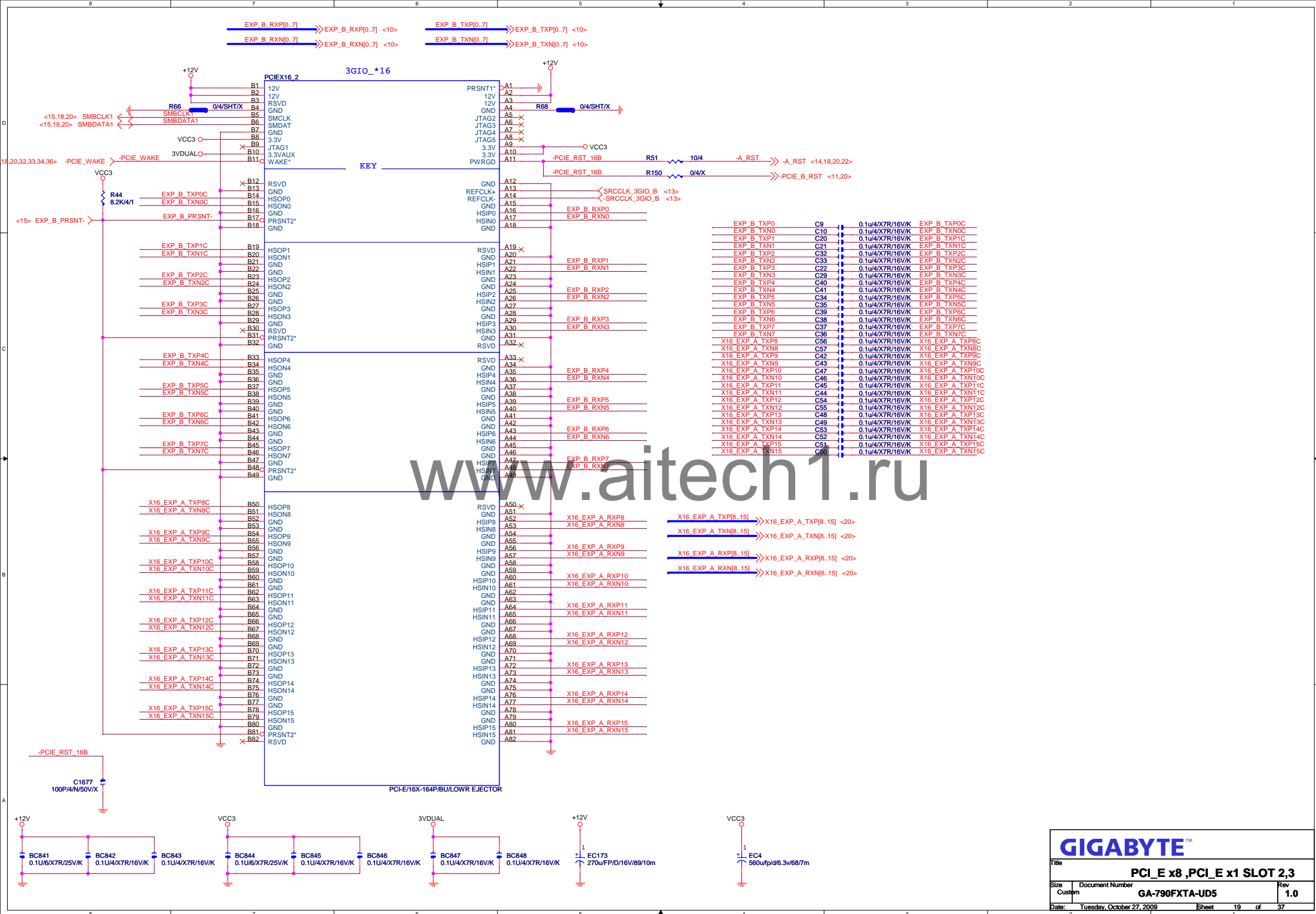
PLACE SATA AC COUPLING
CAPS CLOSE TO SB600



GIGABYTE		
Title		
ATI SB750 SATA/IDE/HWM/SPI		
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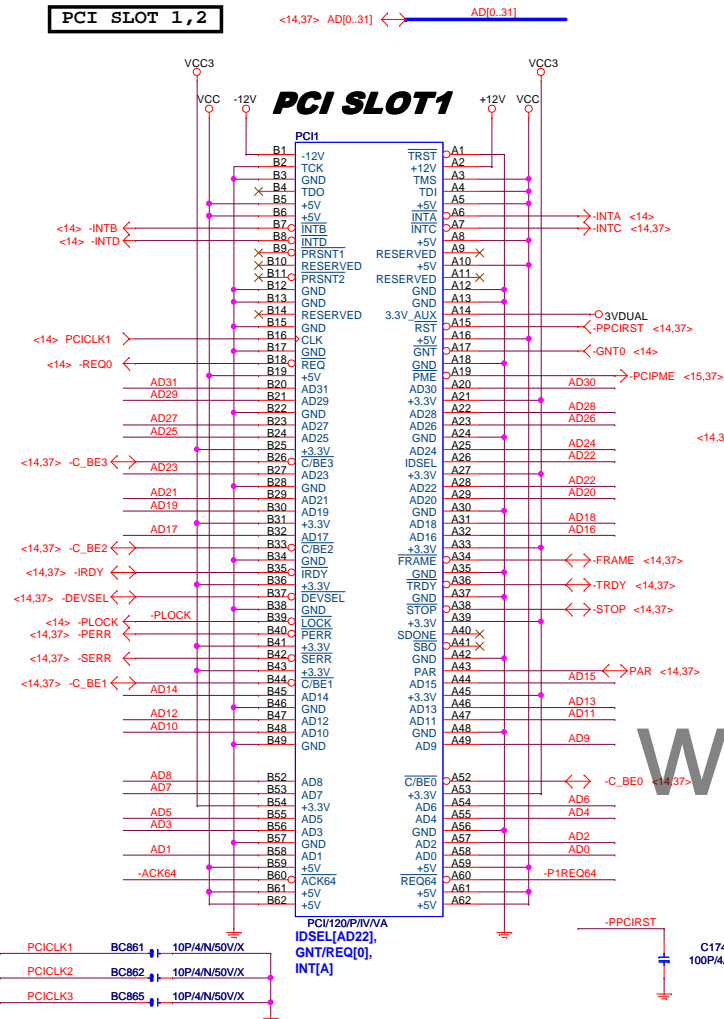




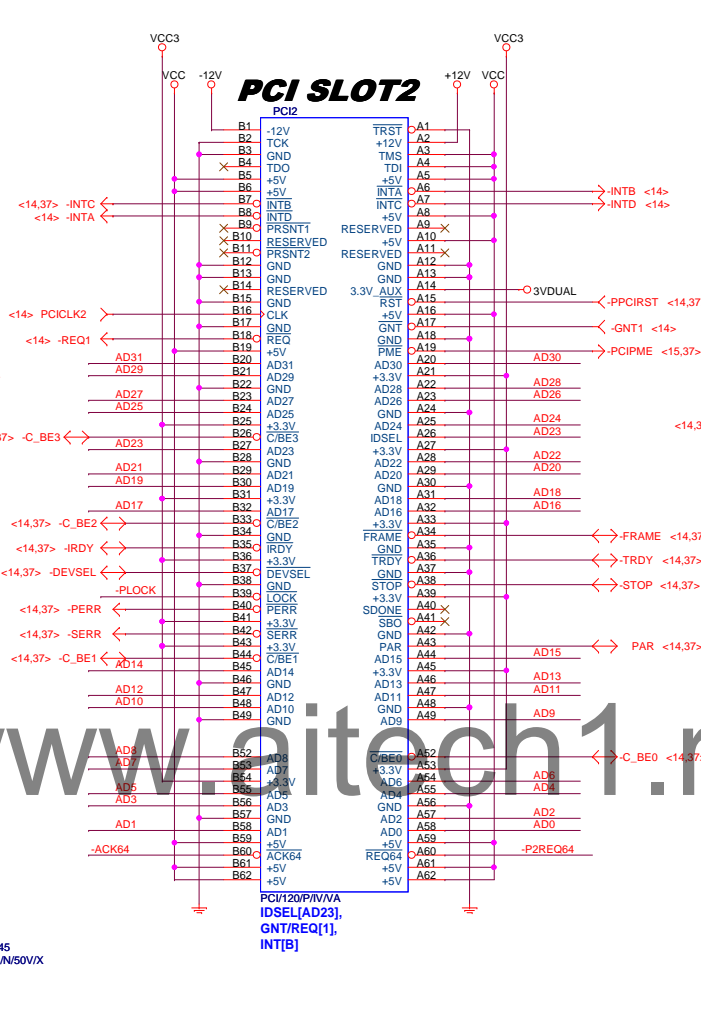
2	1
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PCI SLOT 1,2

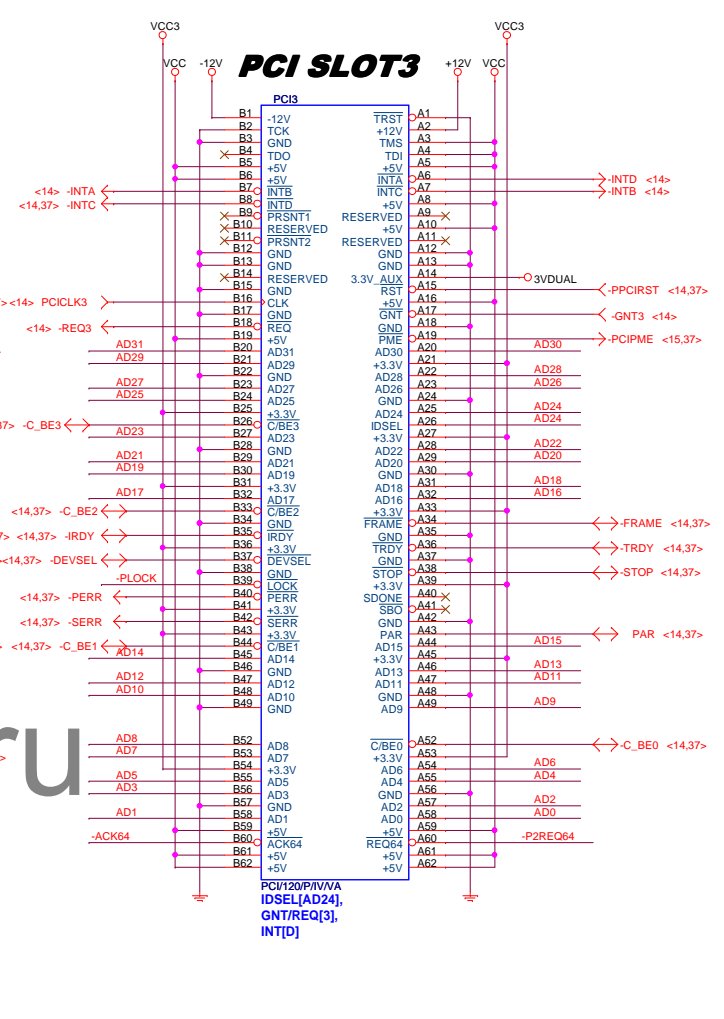
PCI SLOT1



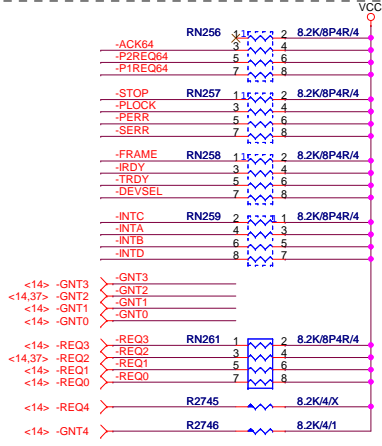
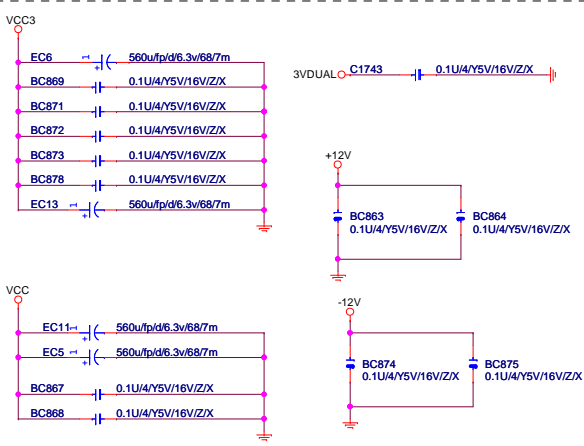
PCI SLOT2



PCI SLOT3



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Title: **PCI SLOT 1,2**

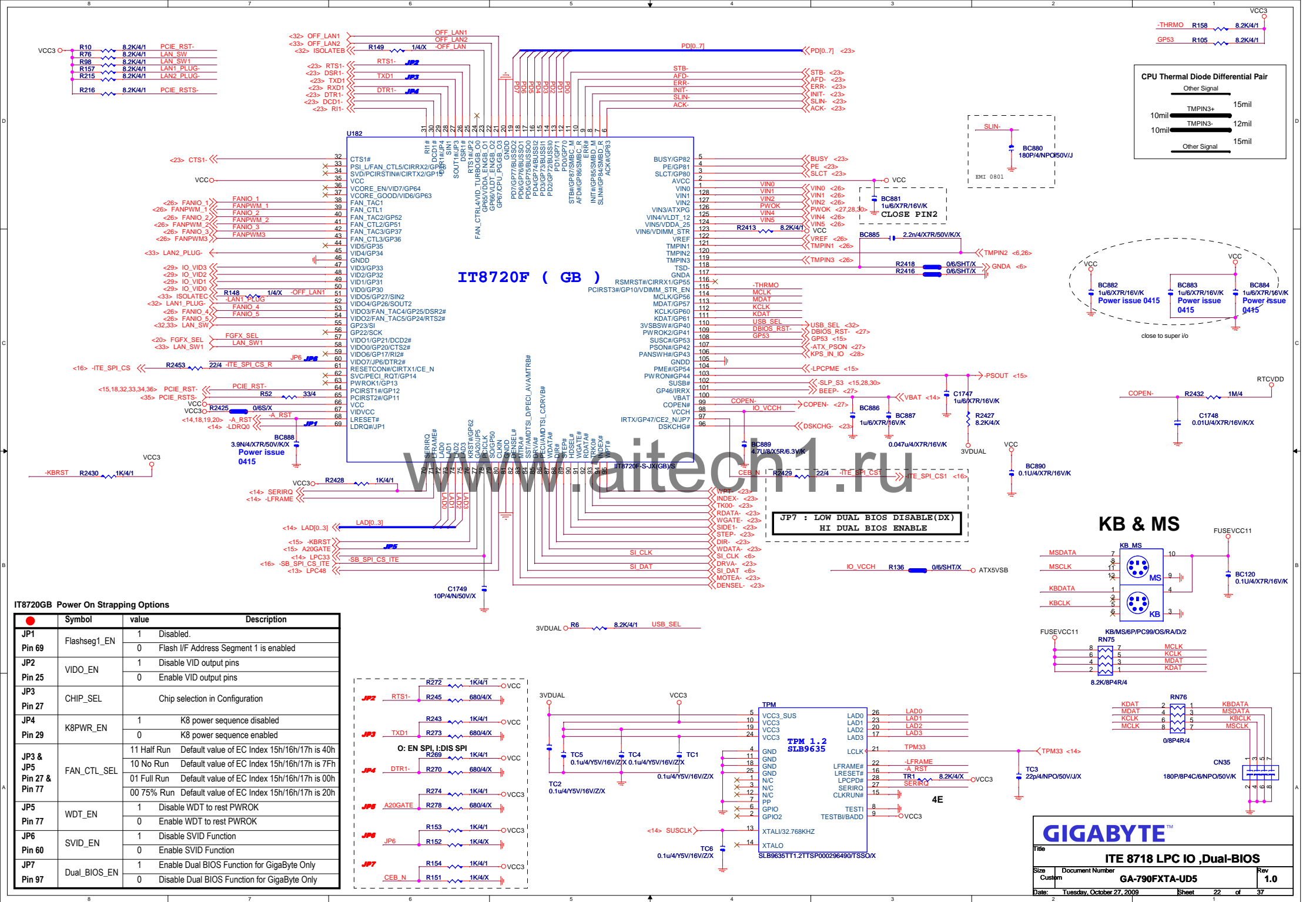
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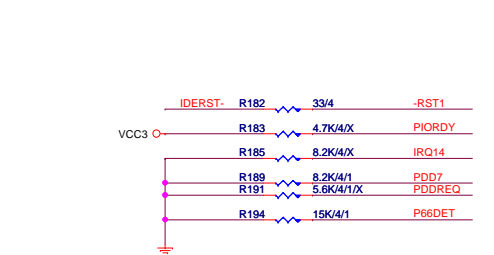
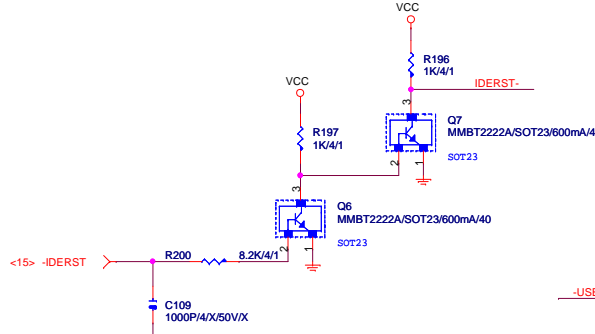
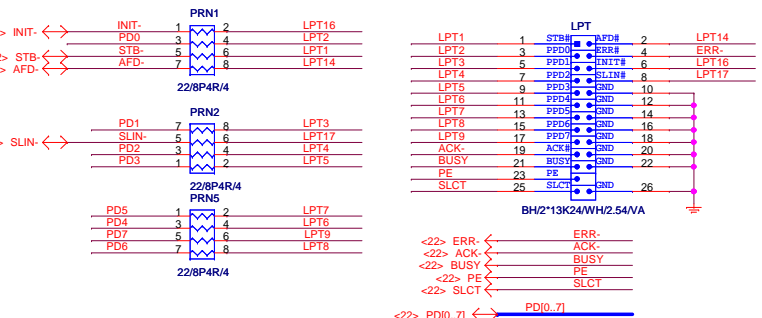
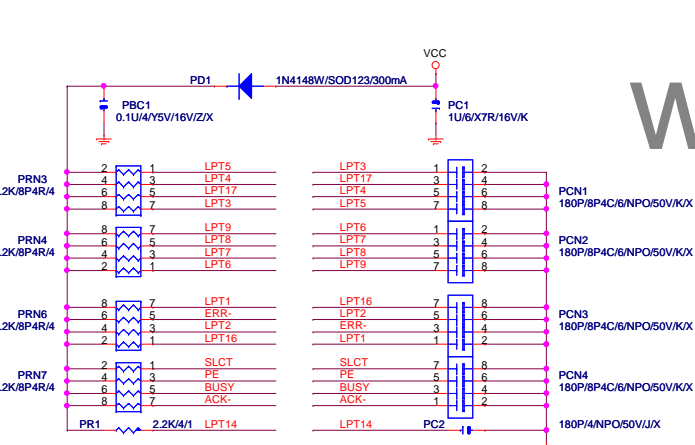
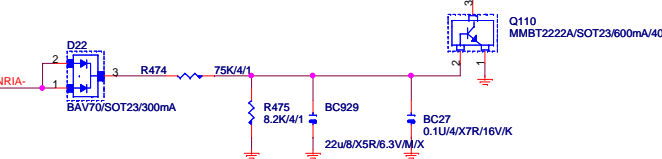
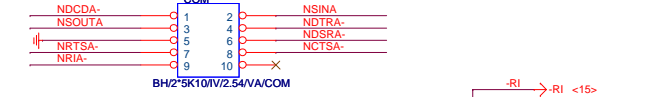
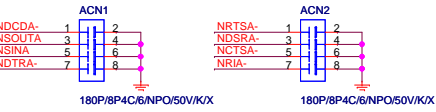
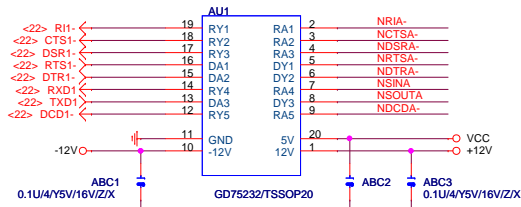
Document Number: **GA-790FXTA-UD5**

Date: Tuesday, October 27, 2009

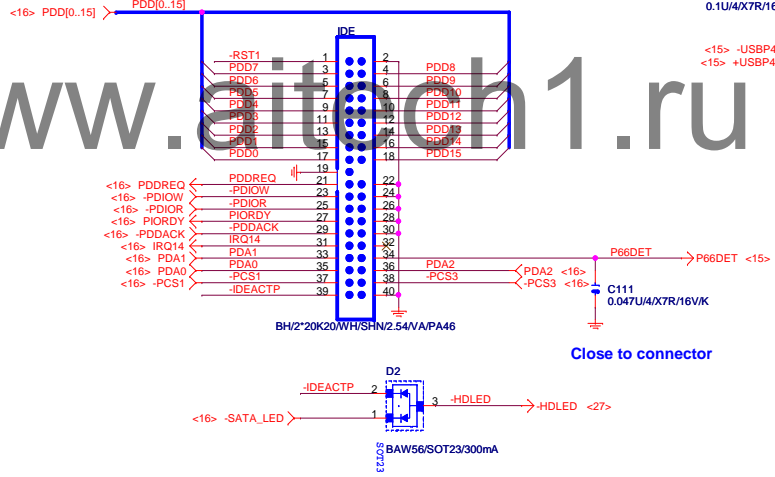
Rev: **1.0**

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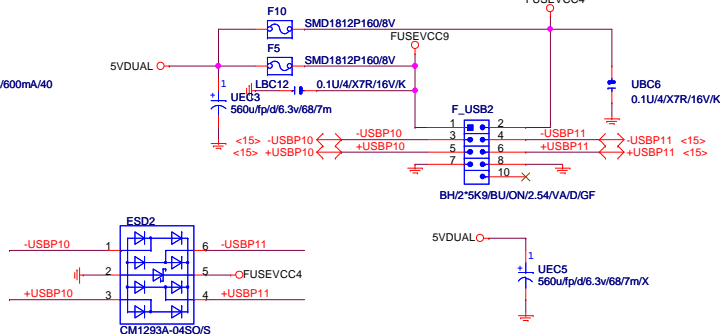




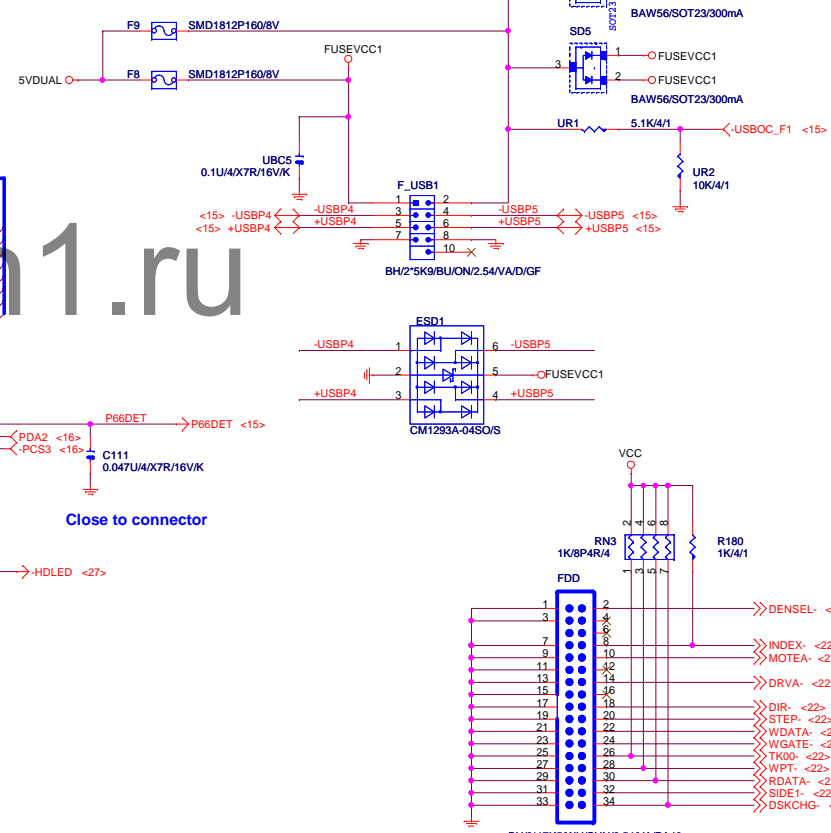
PRIMARY IDE CONNECTOR



FRONT SIDE USB4



FRONT SIDE USB1

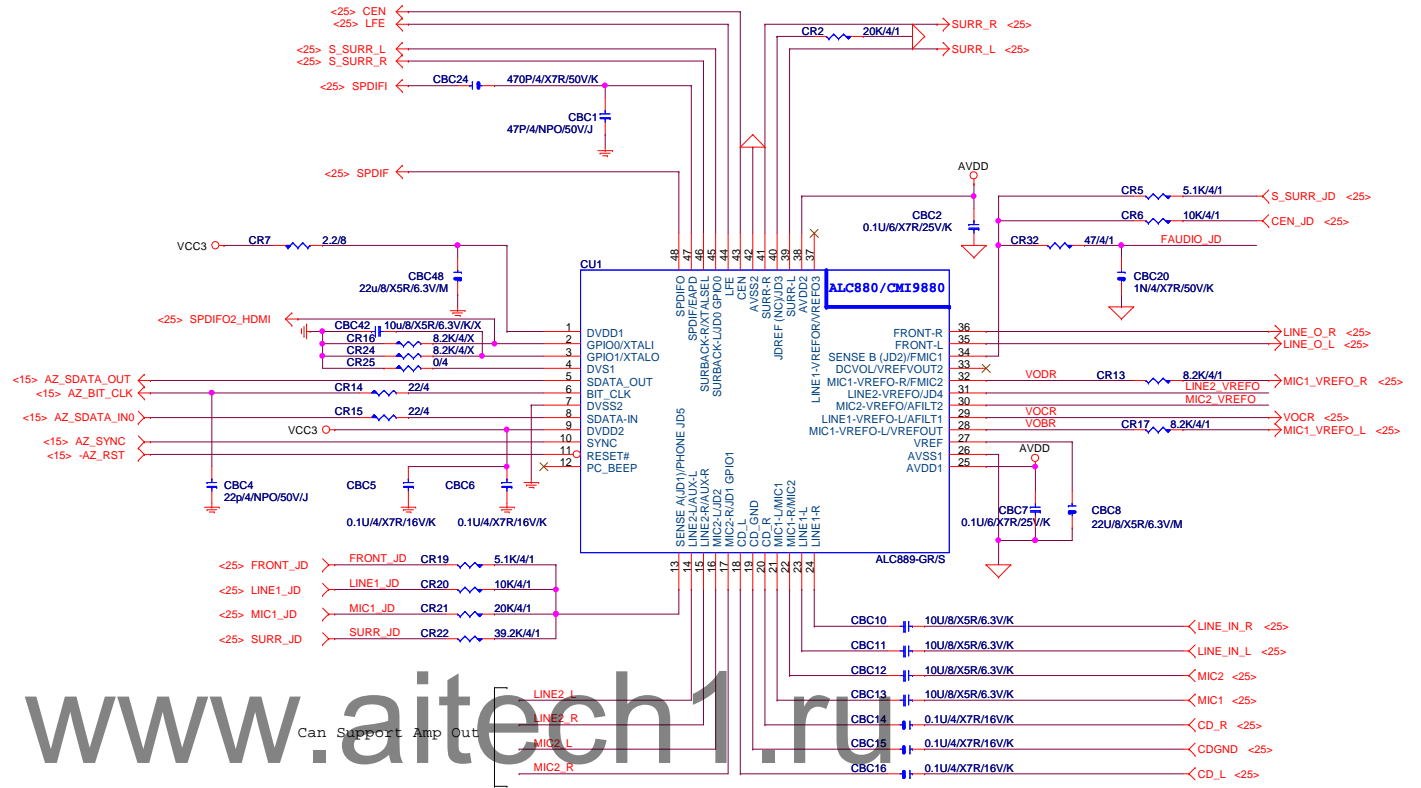


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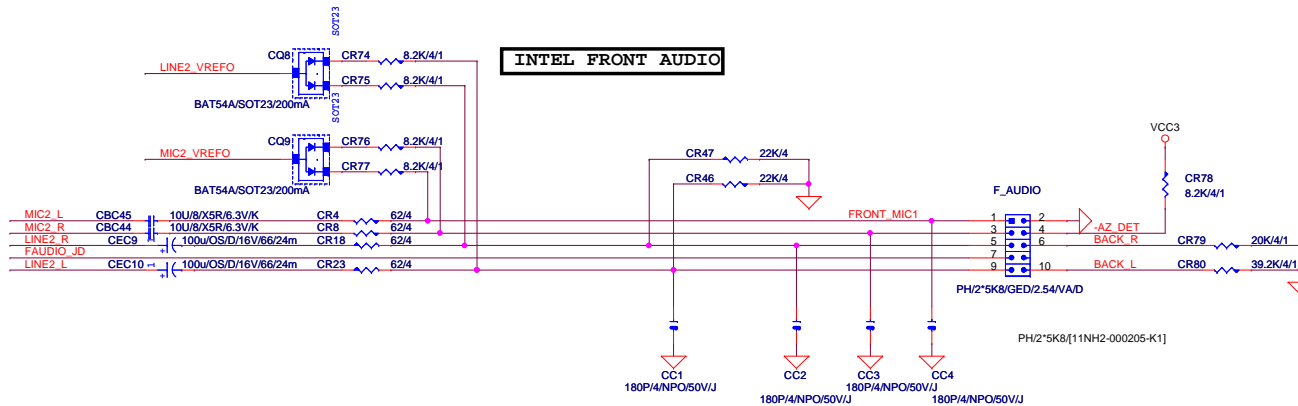
Title		COM/LPT/F_USB	
Size	Document Number	Rev	
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ALC892/ALC889/ Colay

	ALC892	ALC889	ALC889A
CR16	X	X	O
CR24	X	X	O
CR25	X	O	O
CBC42	10uF/X5R	X	X
CR2	20K/1%	20K/1%	20K/0.1%
CR9	O	O	X
CR10	X	X	O
CBC10/CBC11/CBC12/ CBC13/CBC44/CBC45	4.7uF /X5R	10uF /X5R	4.7uF /X5R
CR4/CR8/CR18/CR23/ CR11/CR12/CR27/CR29/ CR49/CR50/CR43/CR44/ CR45/CR48/CR59/CR60	75 ohm or lower	66 ohm or lower	75 ohm

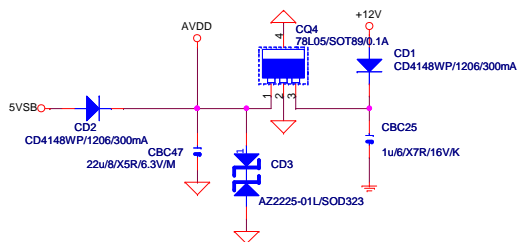
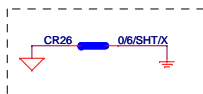


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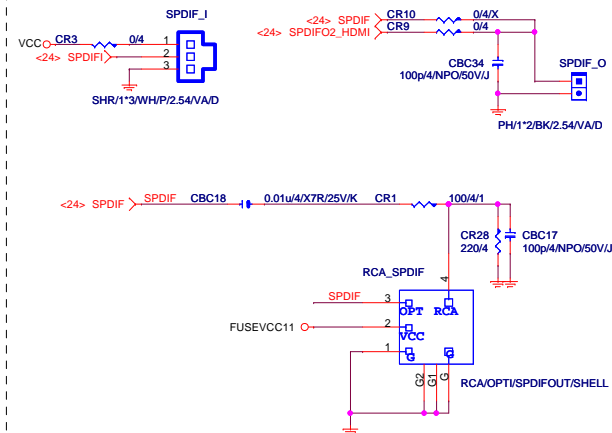


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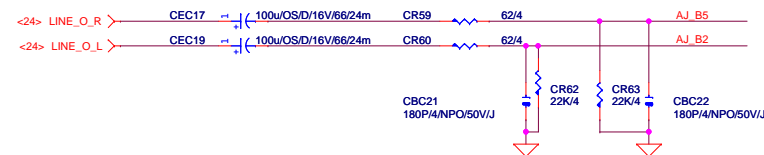
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ALC889A CODEC		
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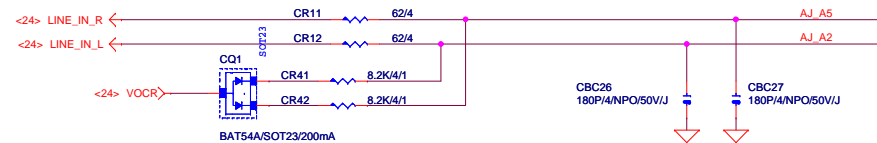
SPDIF



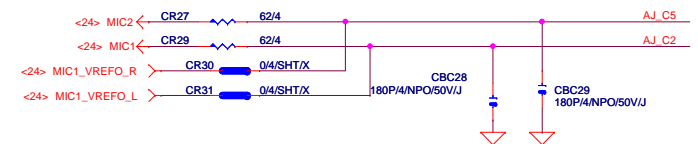
LINE OUT FRONT OUT



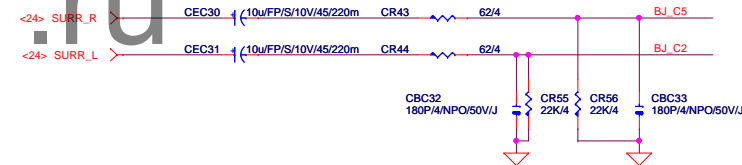
LINE-IN



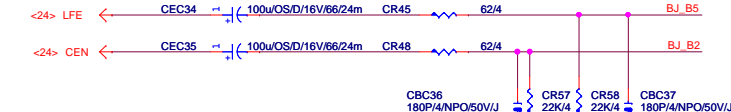
MIC



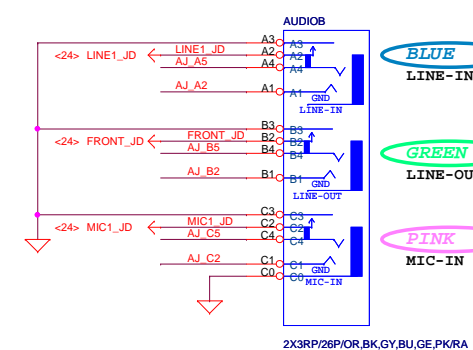
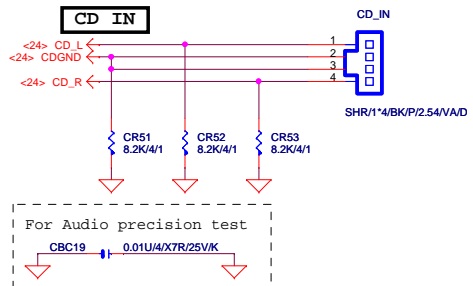
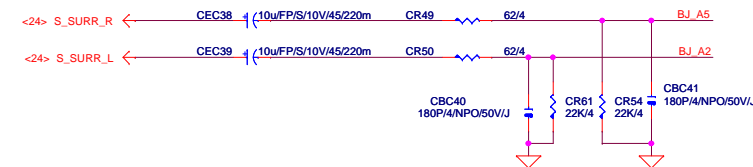
SURROUND



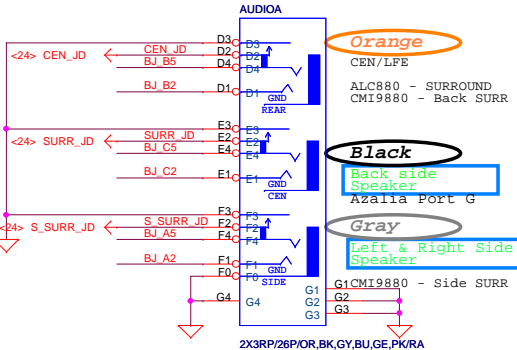
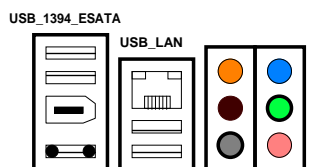
CEN/LFE



SURR BACK



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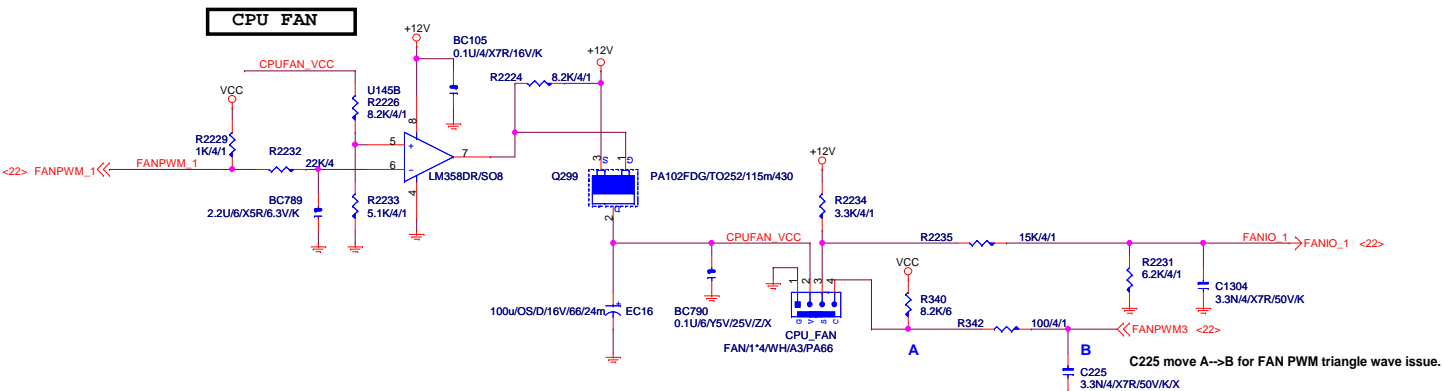
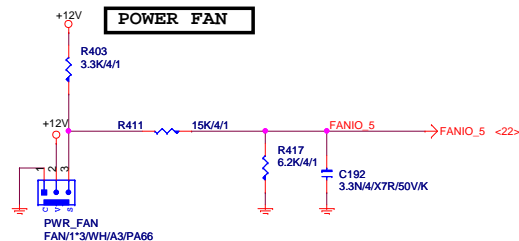
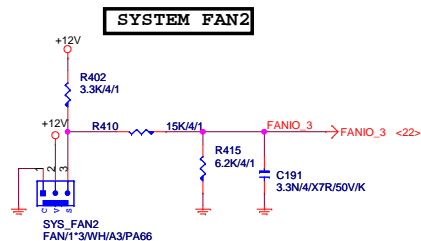
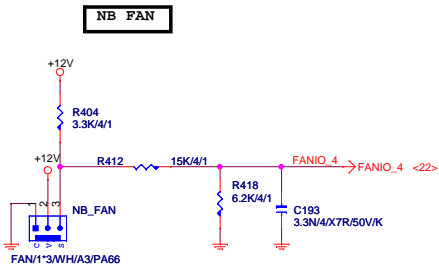
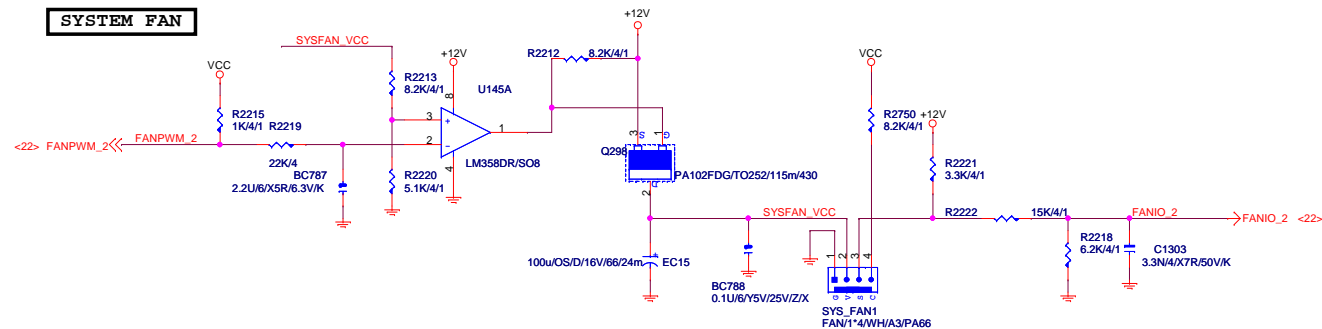
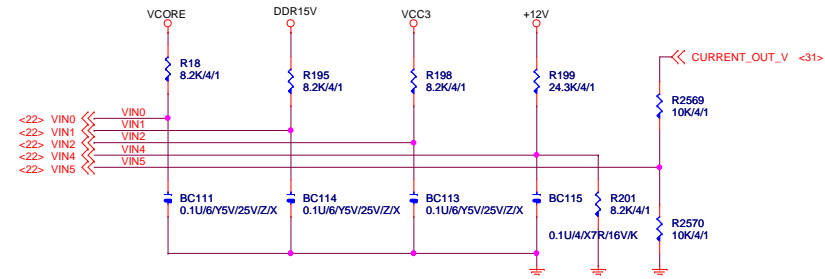
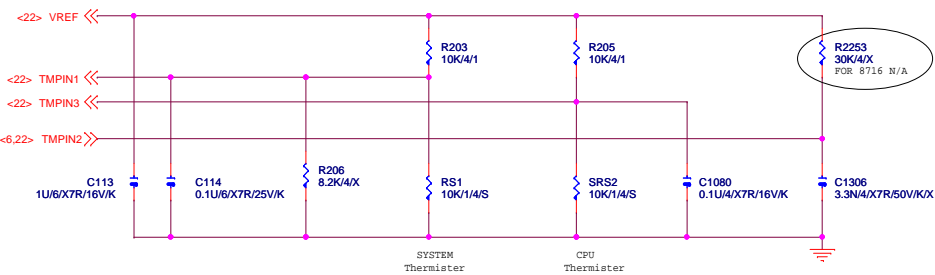
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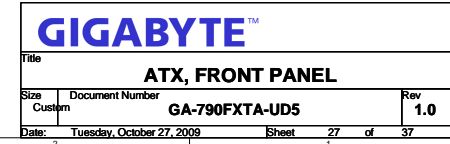
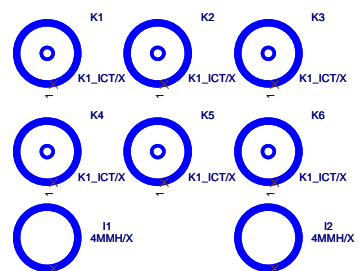
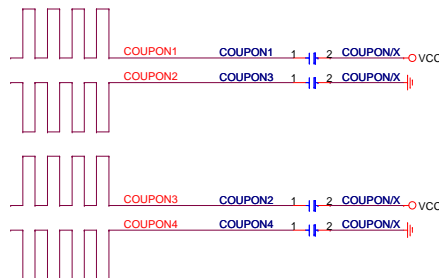
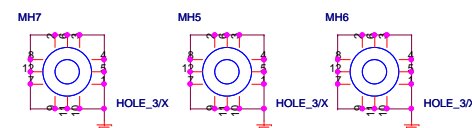
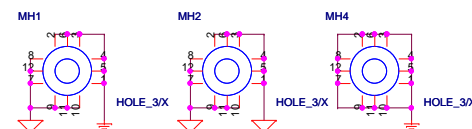
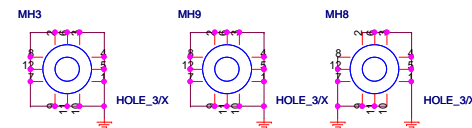
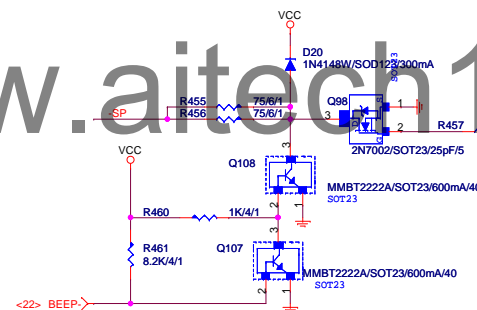
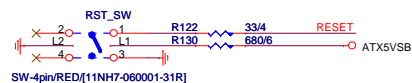
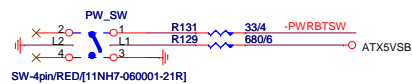
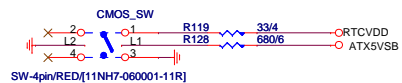
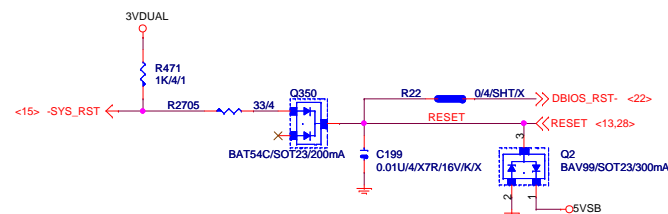
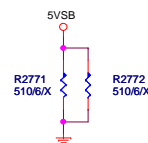
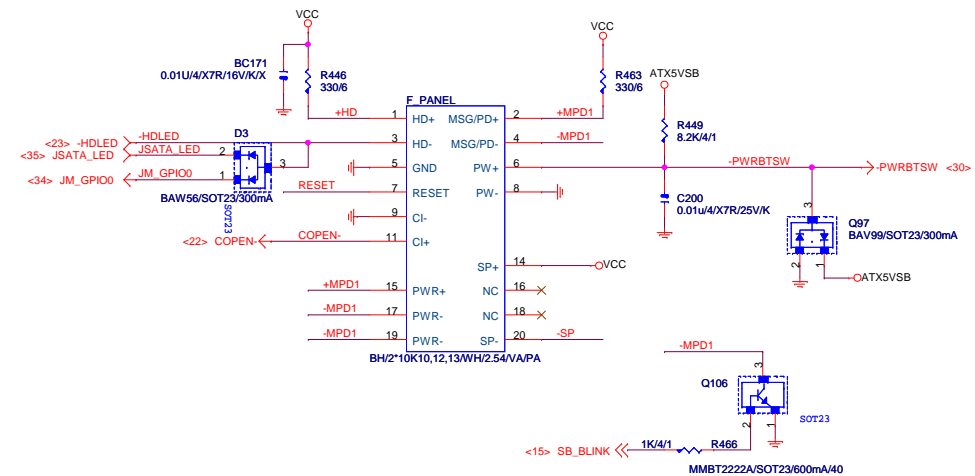
Hardware Monitor circuits

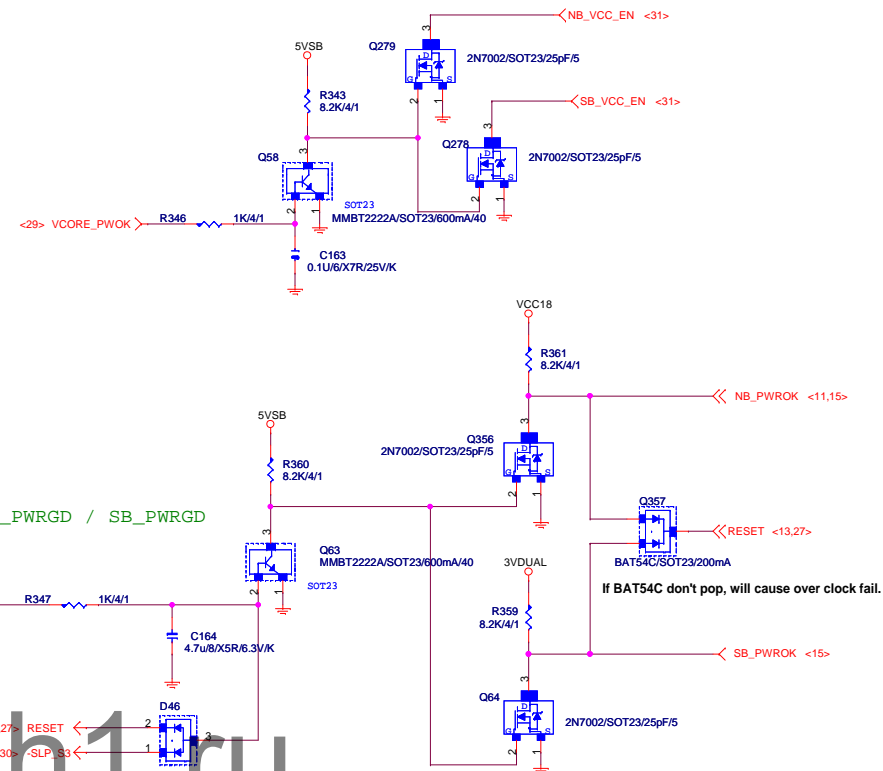
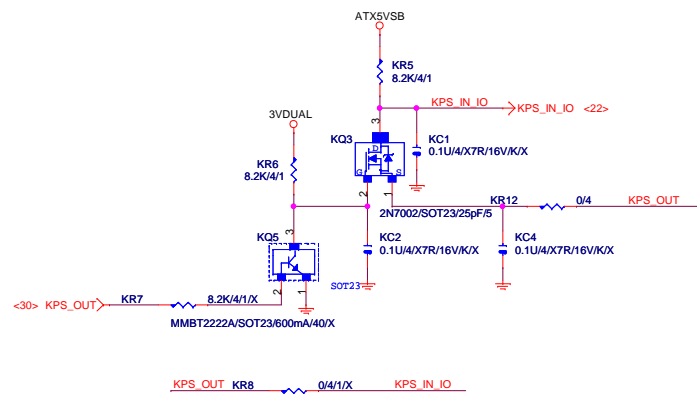
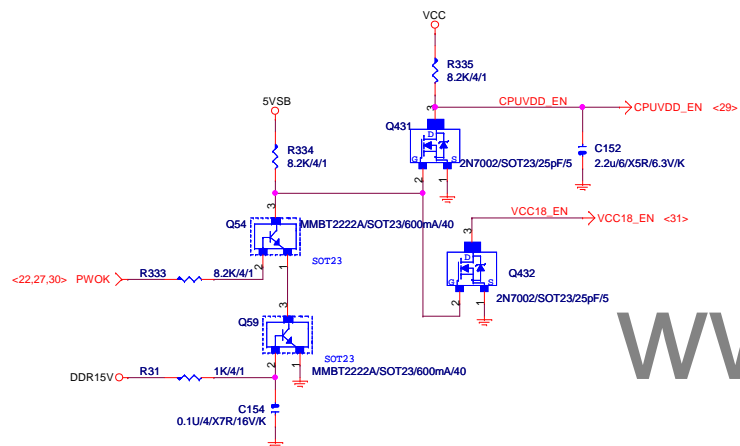
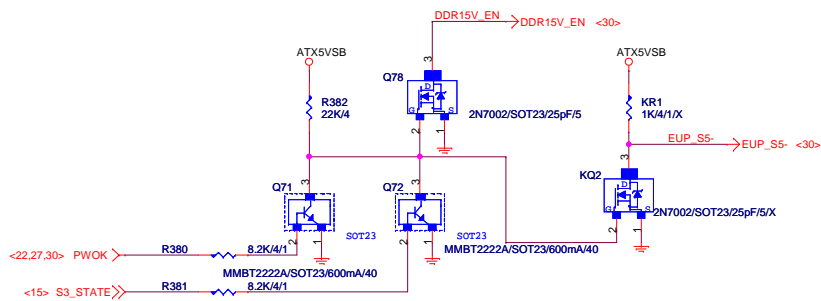


C225 move A-->B for FAN PWM triangle wave issue.

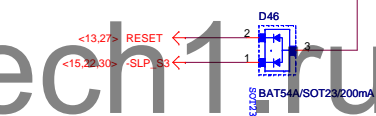
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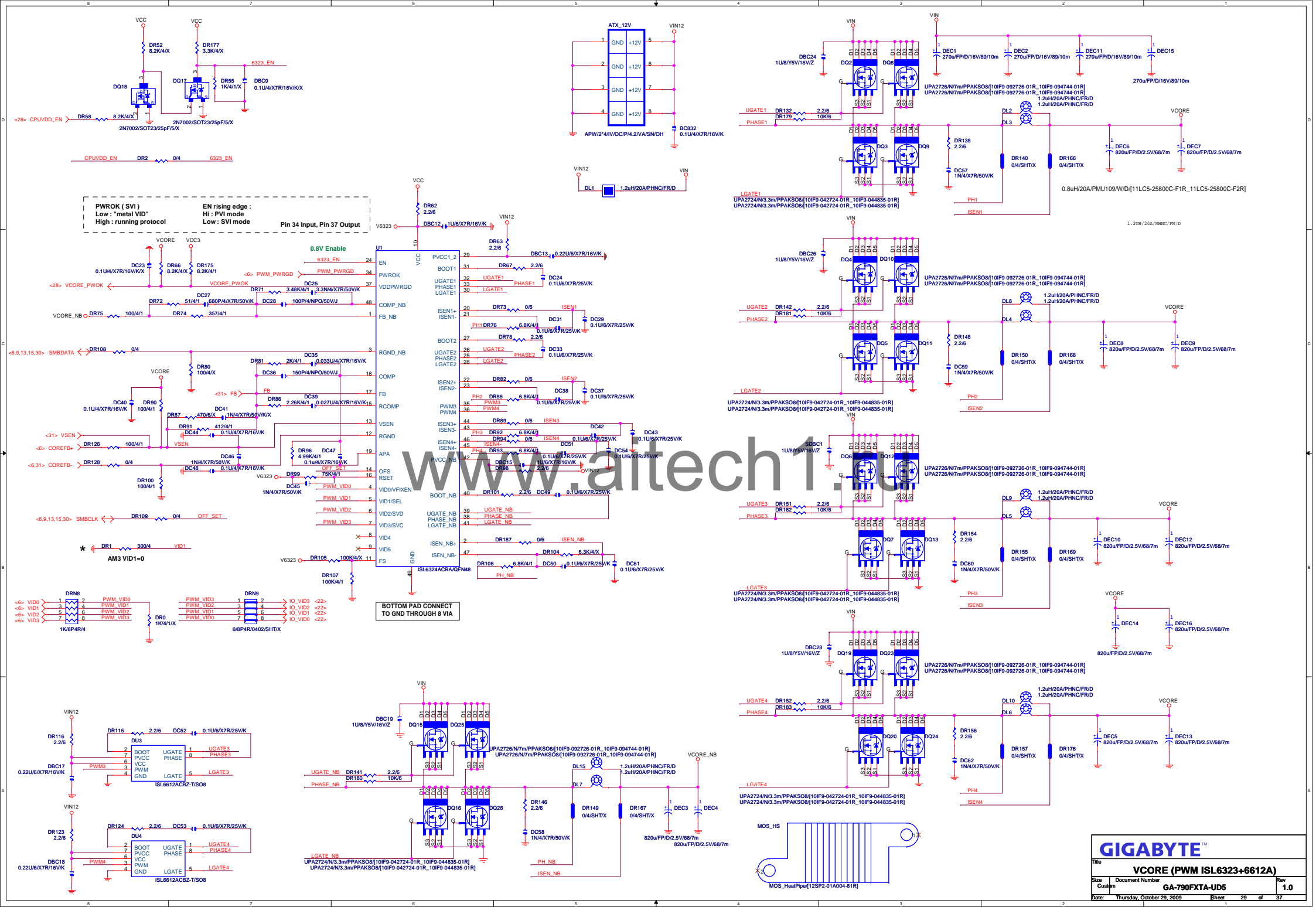


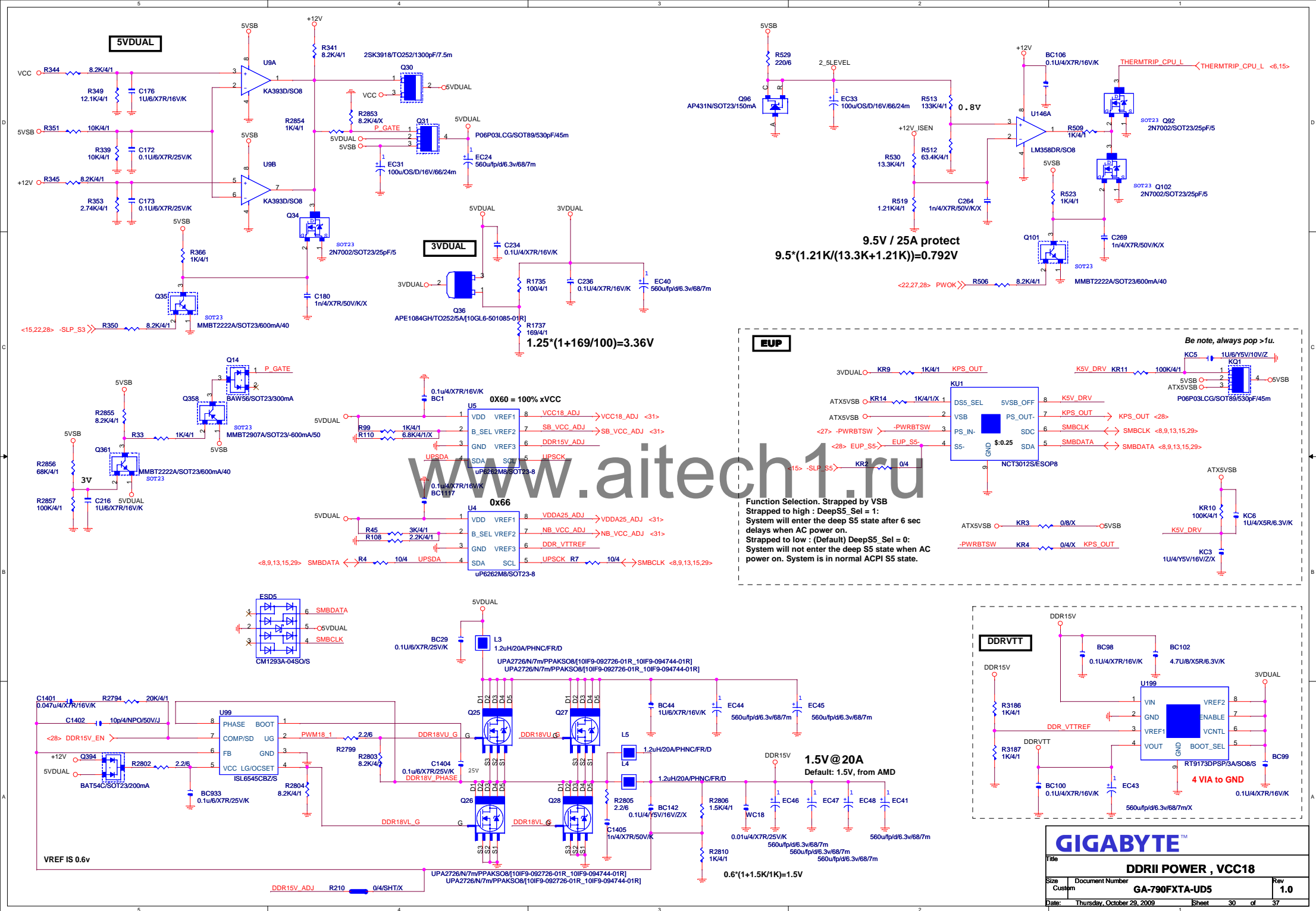
PWOK > NB_PWRGD / SB_PWRGD



(1.8V , 1.2V , 1.1V) > NB_PWRGD 前 1ms

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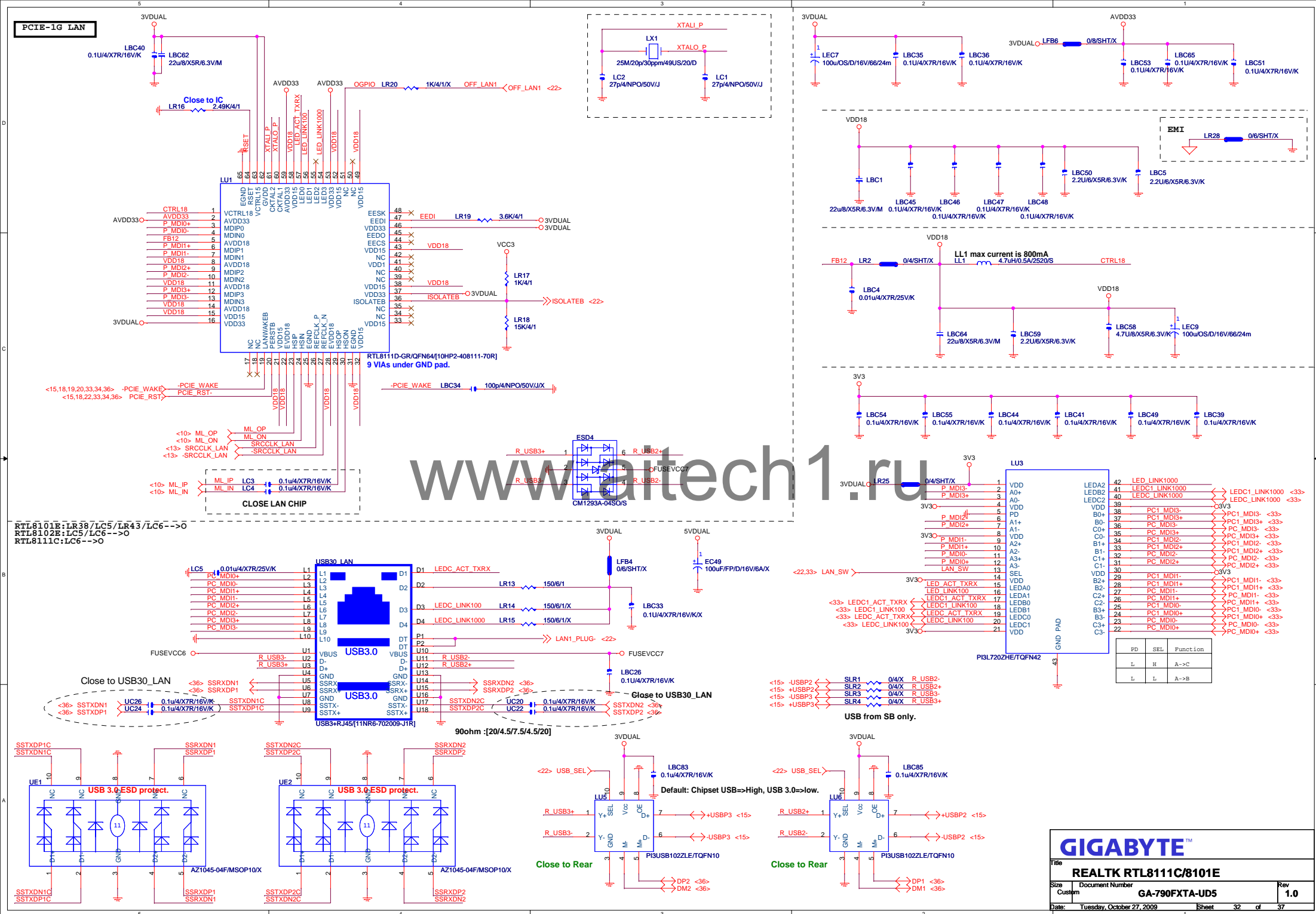
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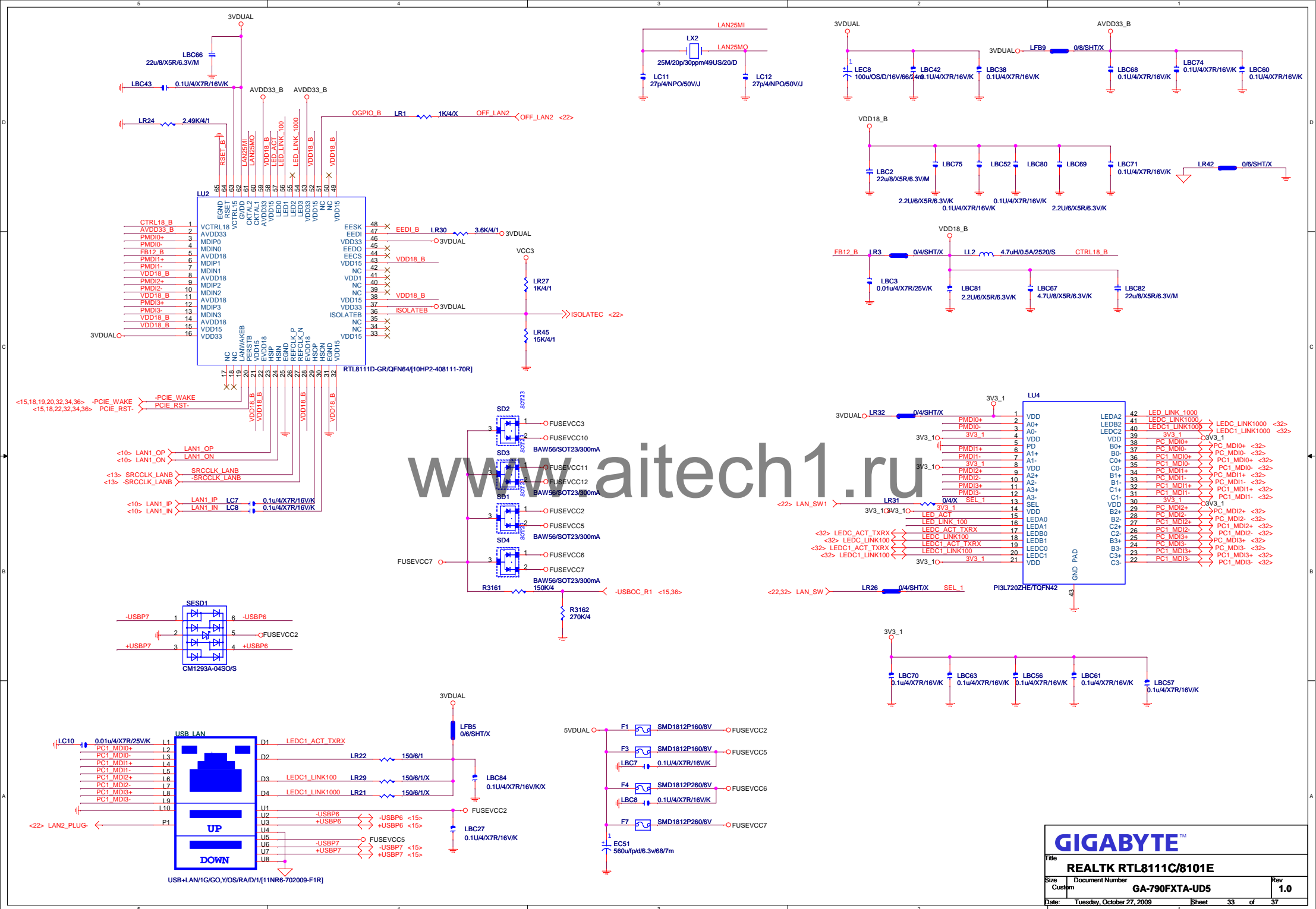
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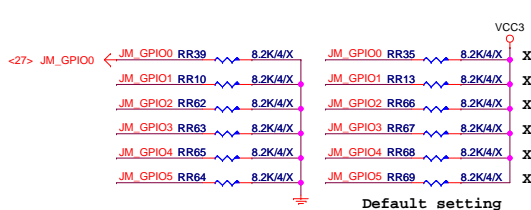
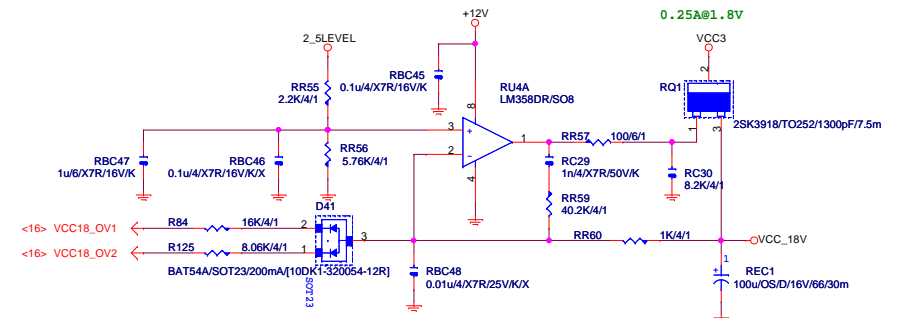
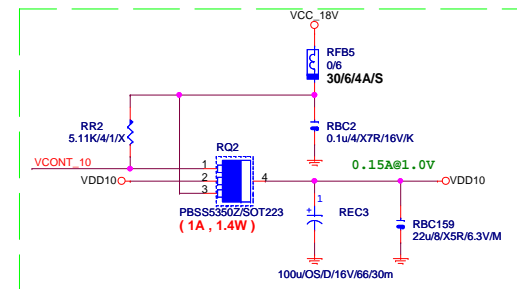
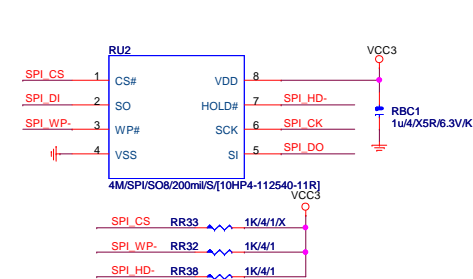
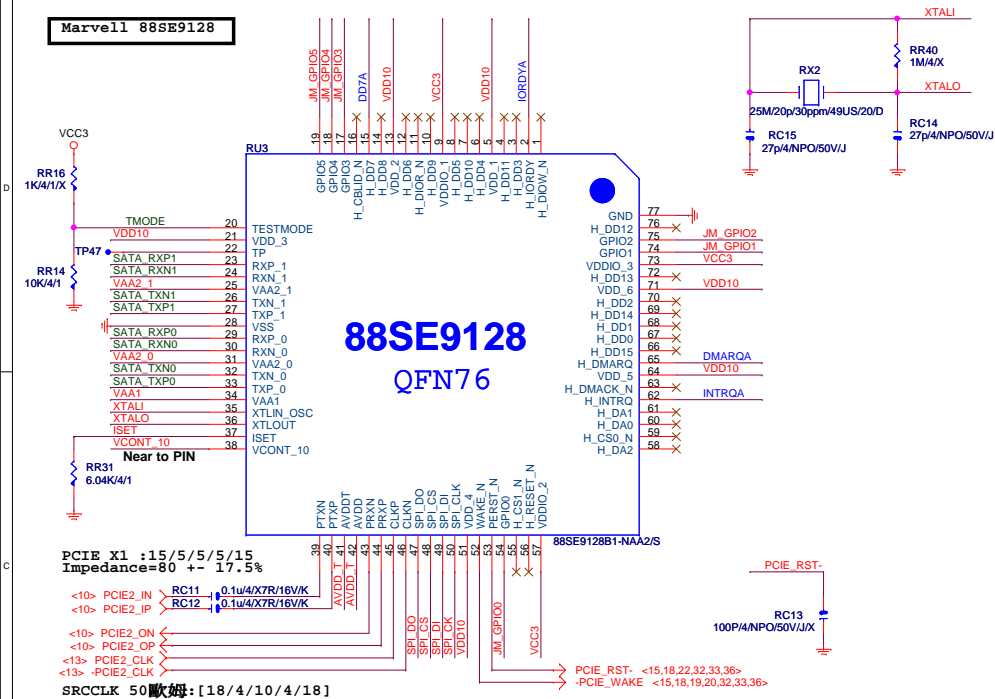
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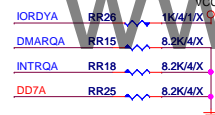
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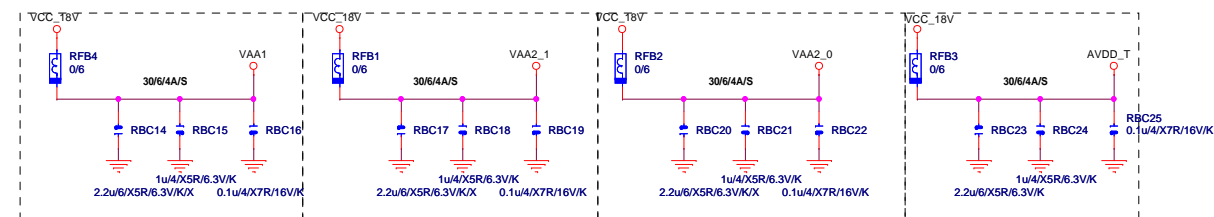
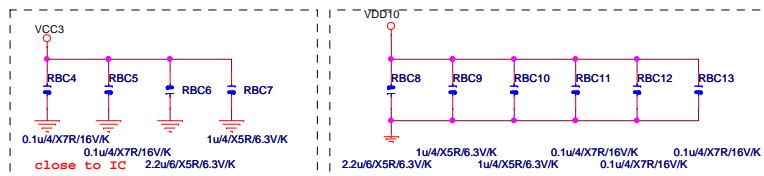
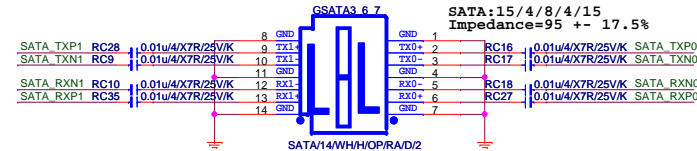




Default setting

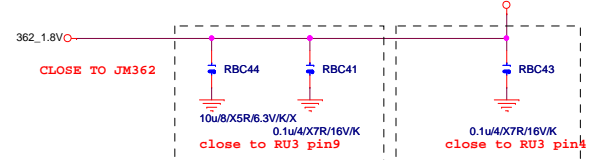
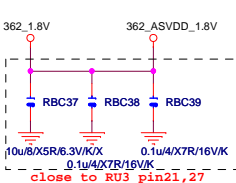
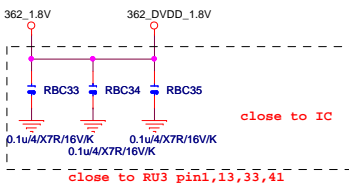
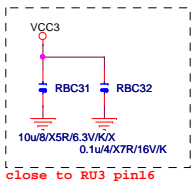
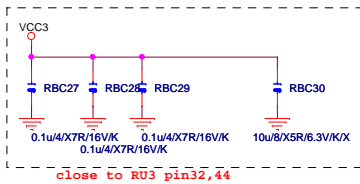
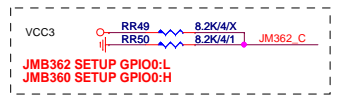
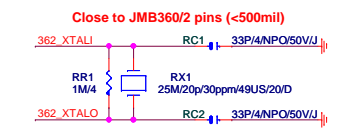
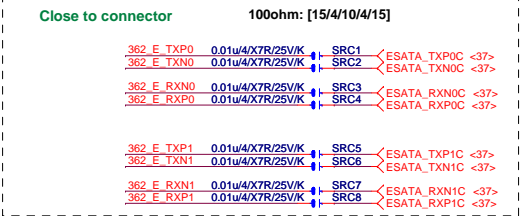
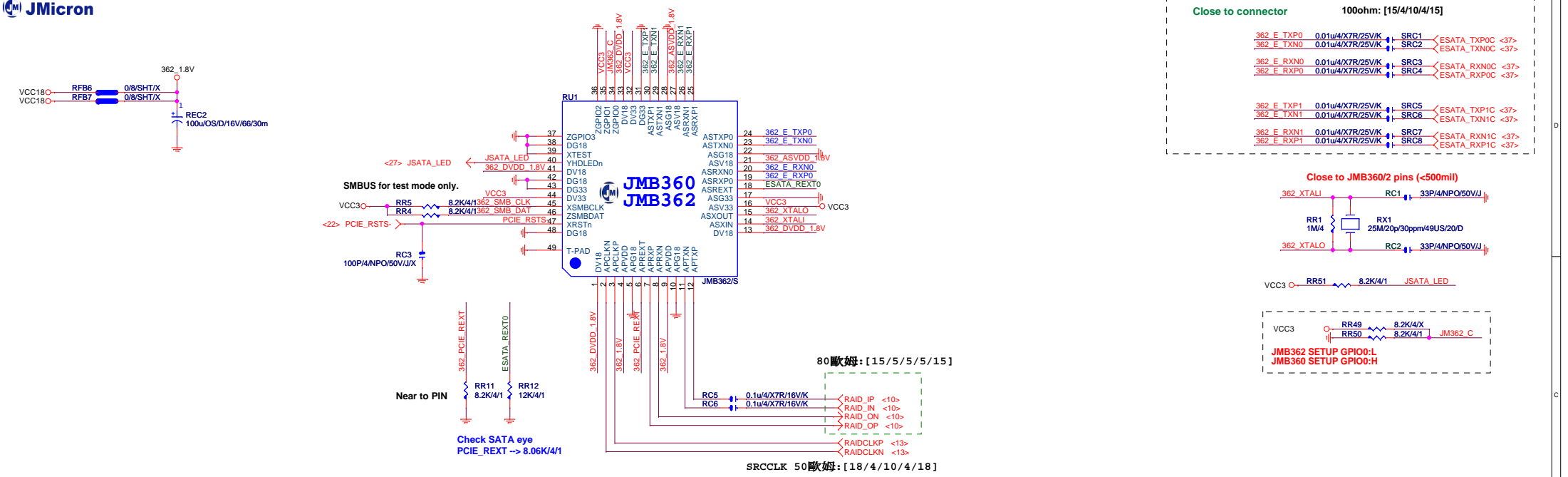


SATA Connector



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